

Fig. 1

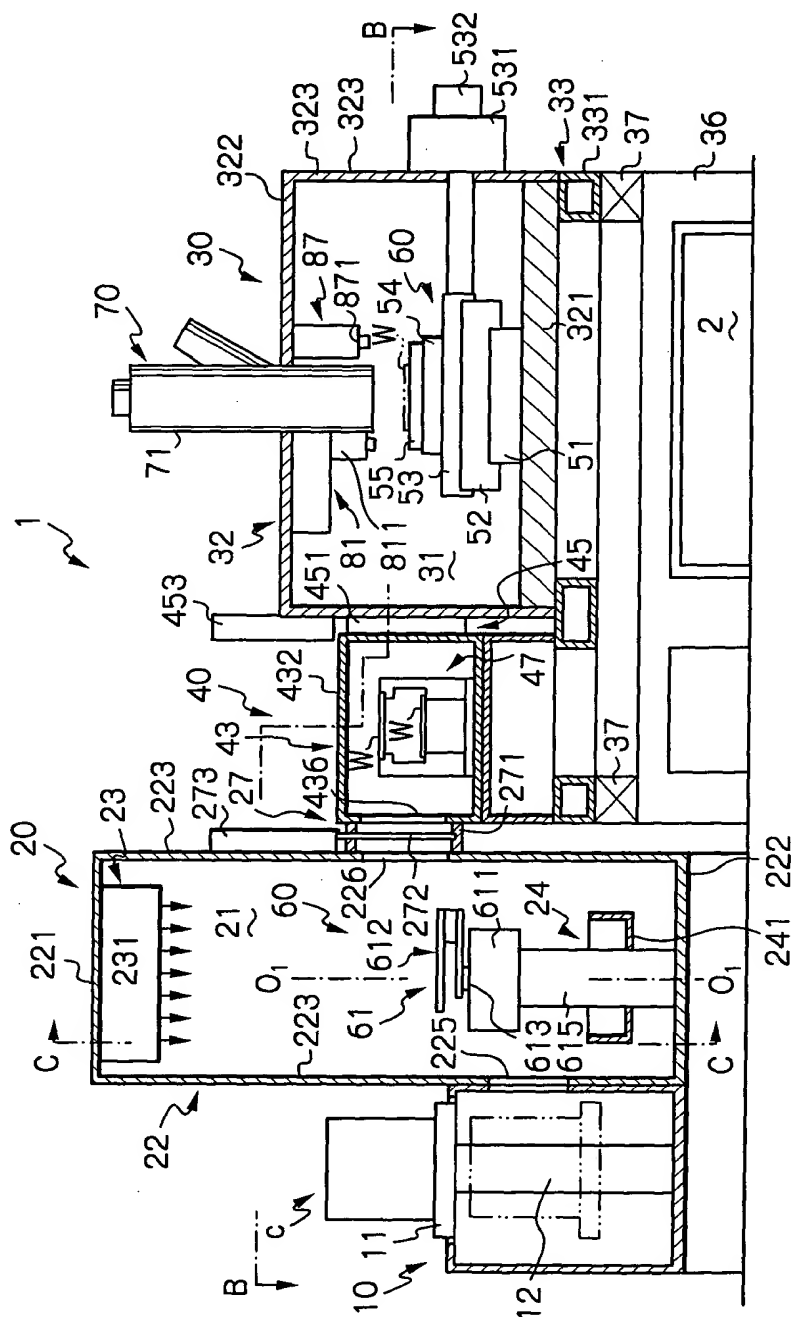
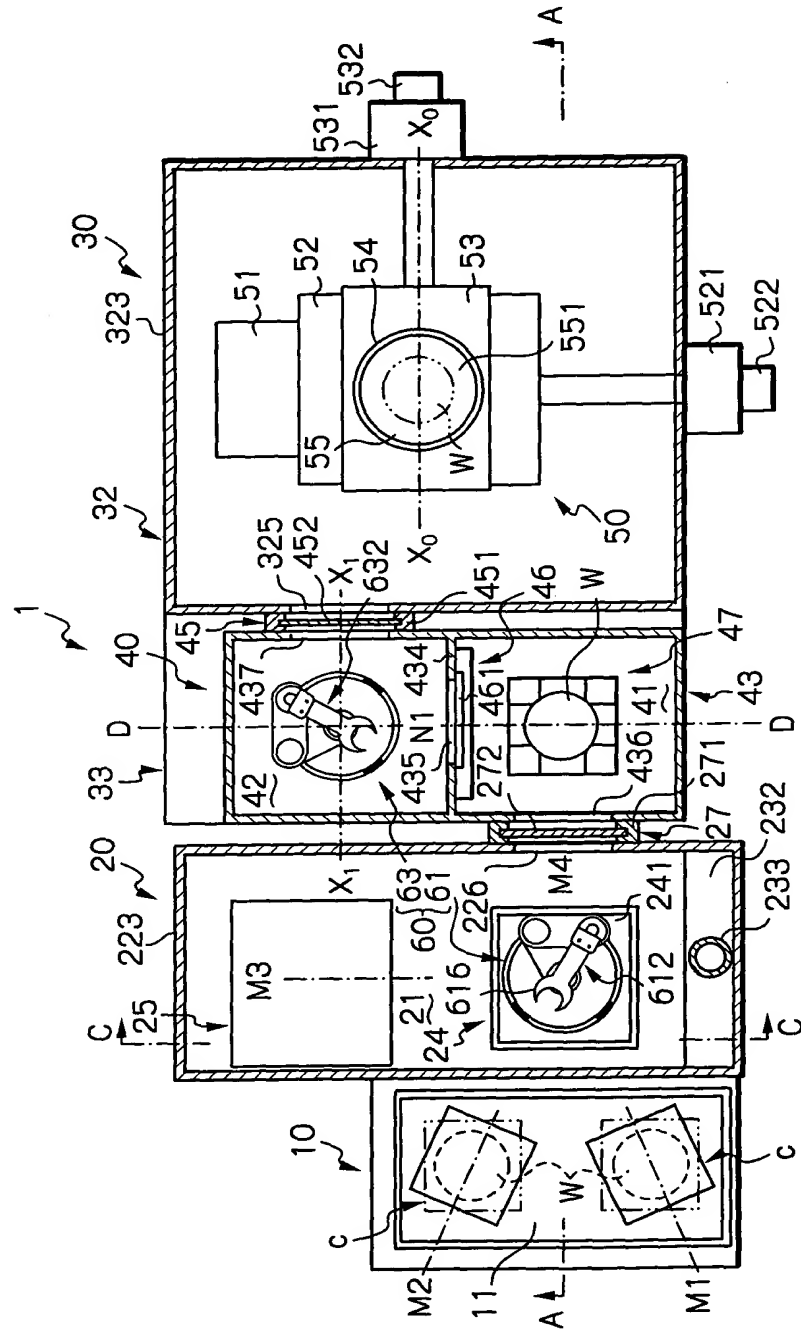


Fig. 2 A



639/0

*Fig. 2 B*

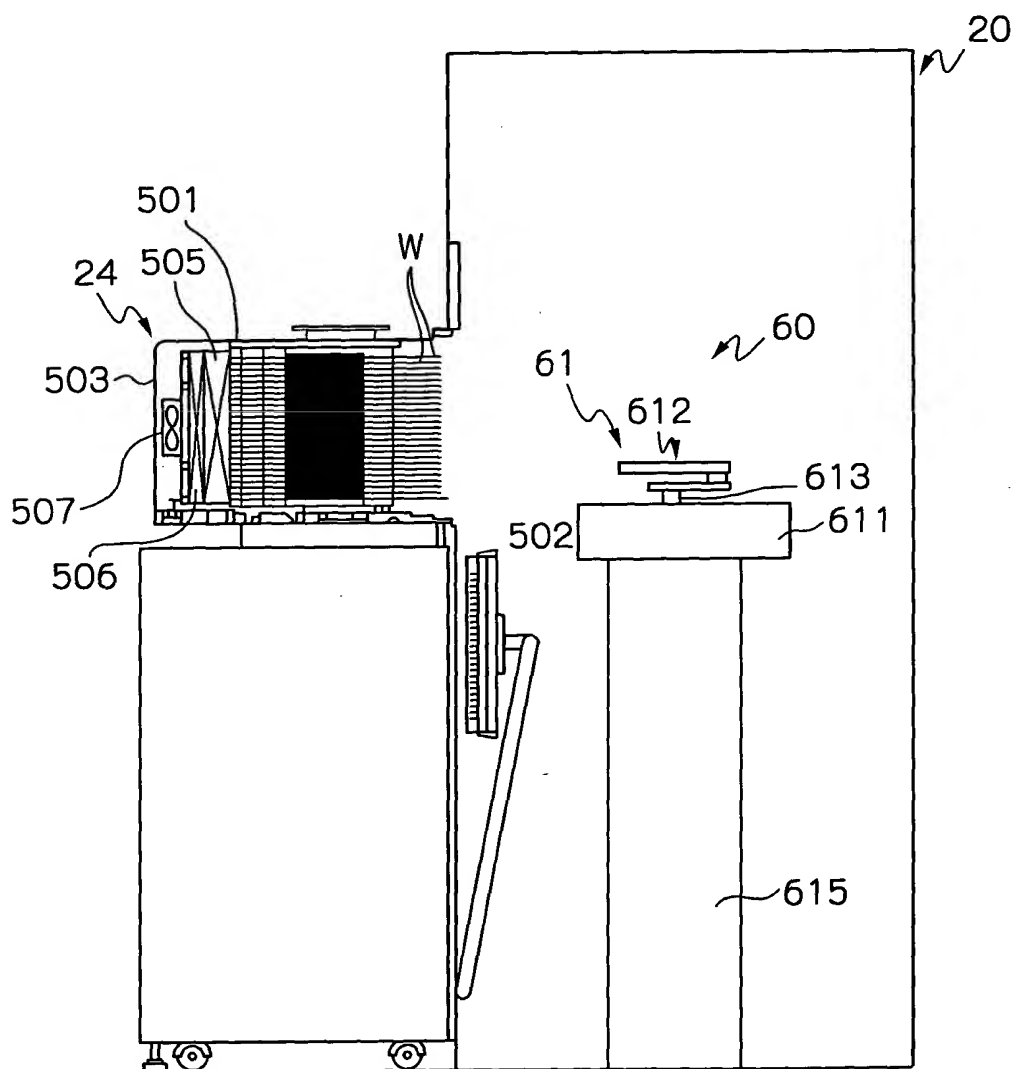
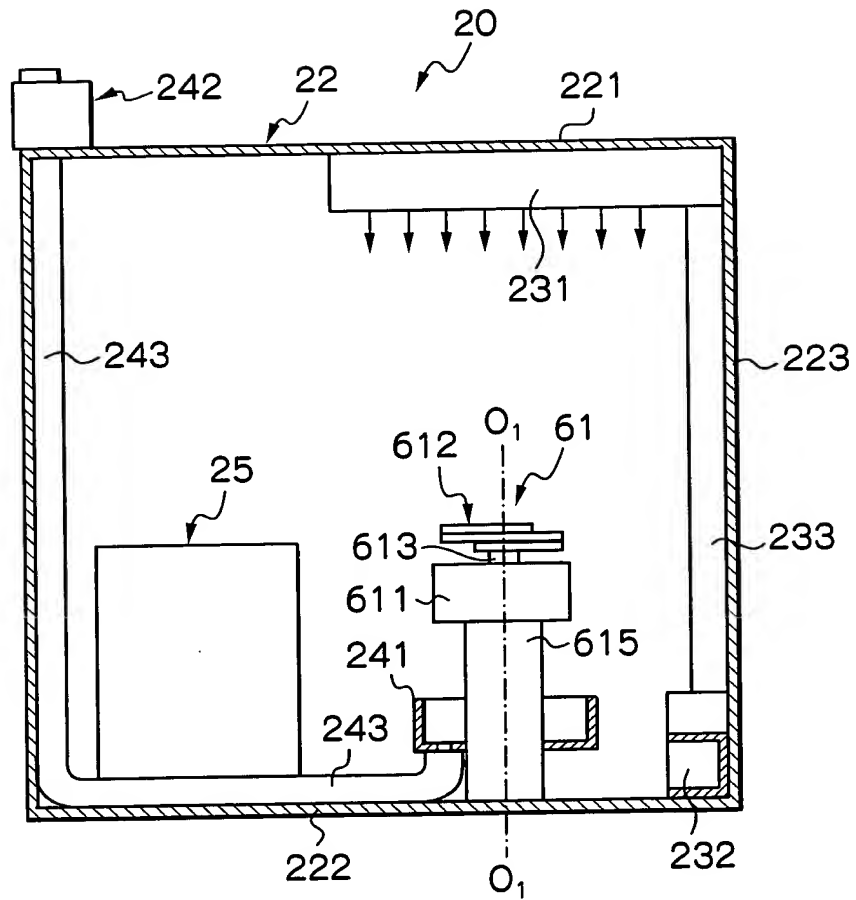


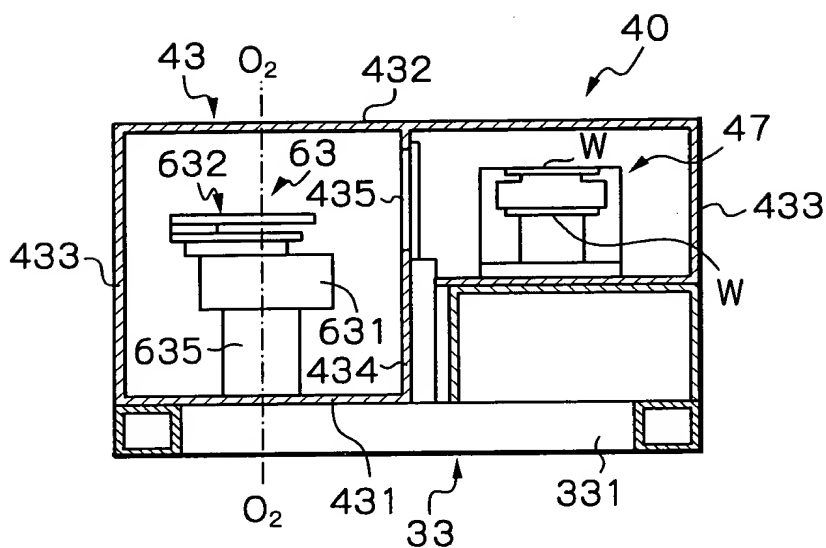


Fig. 3

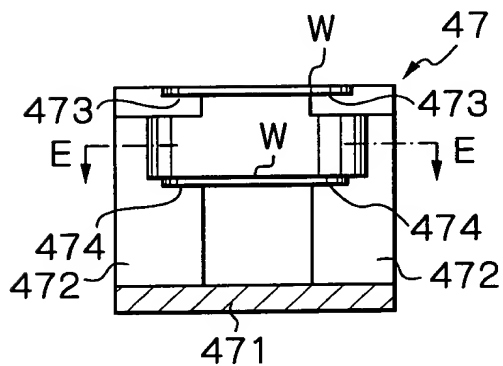




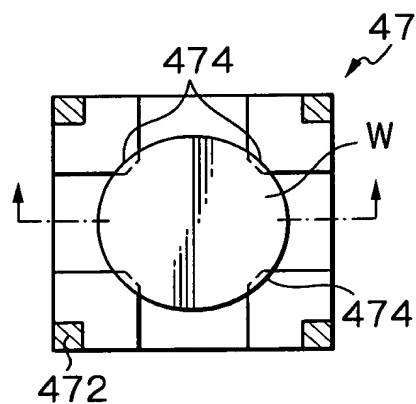
*Fig. 4*



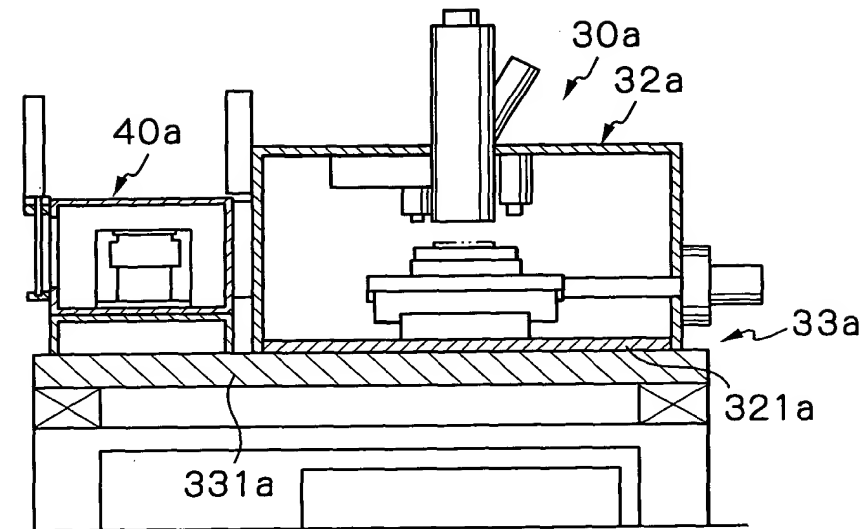
*Fig. 5 A*



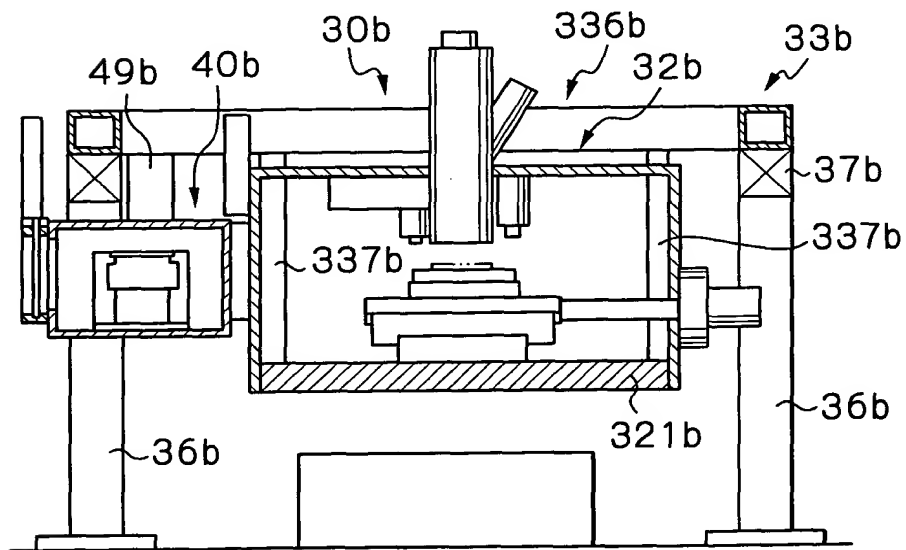
*Fig. 5 B*



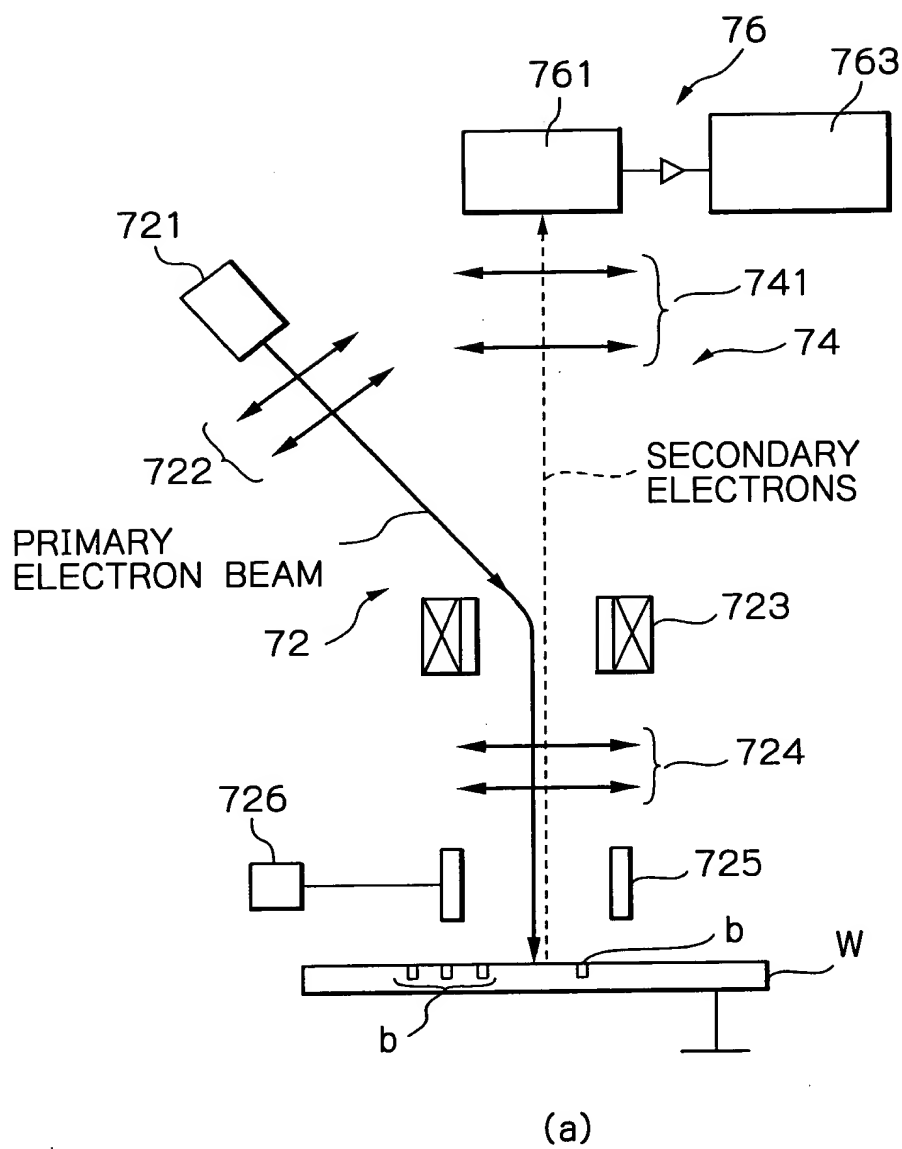
*Fig. 6*



*Fig. 7*

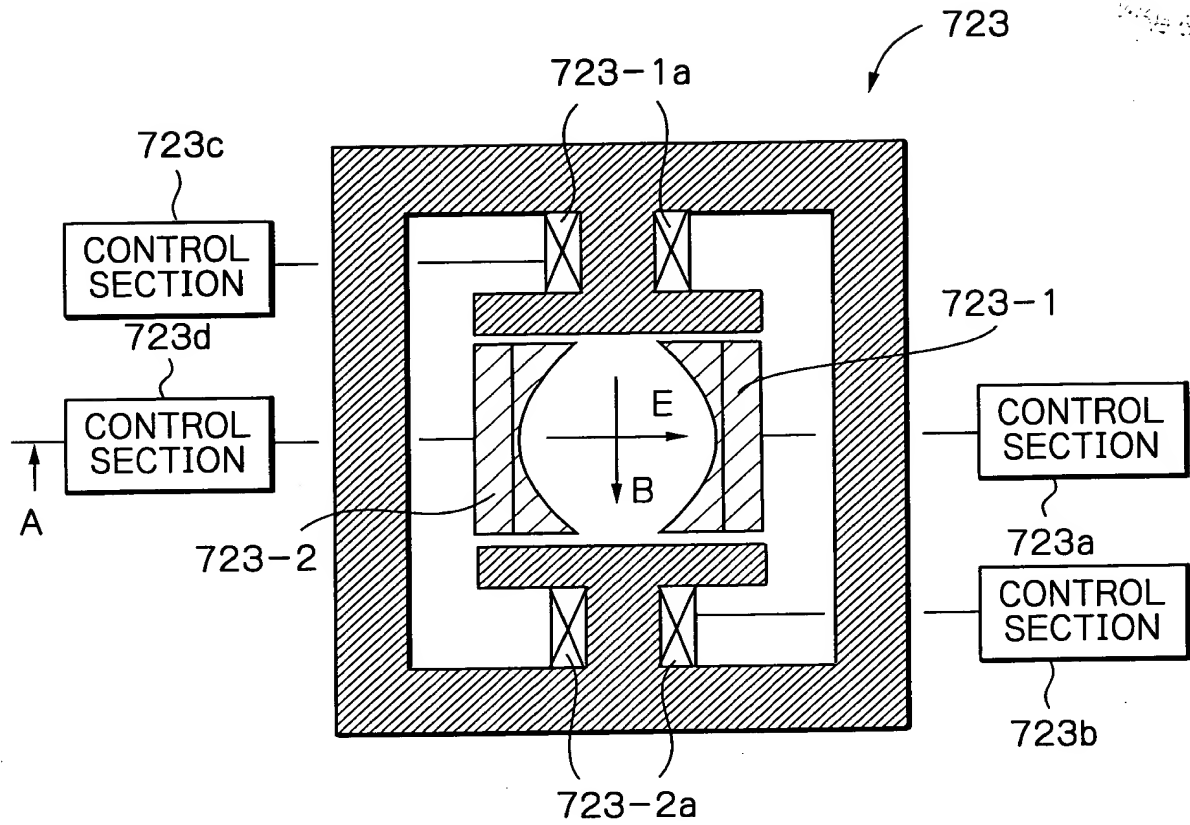


*Fig. 8*

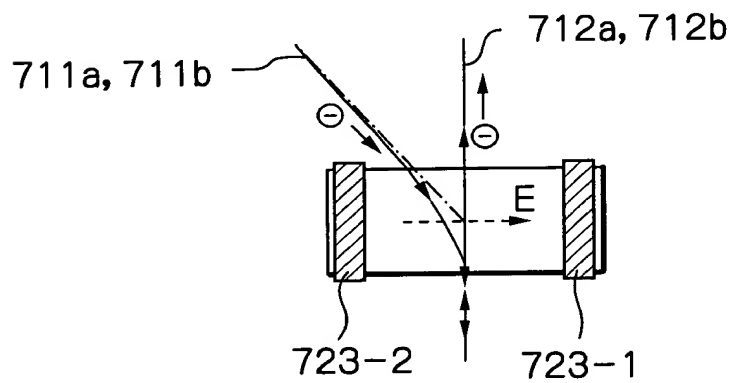


2024-11-14 10:20:41

*Fig. 9*

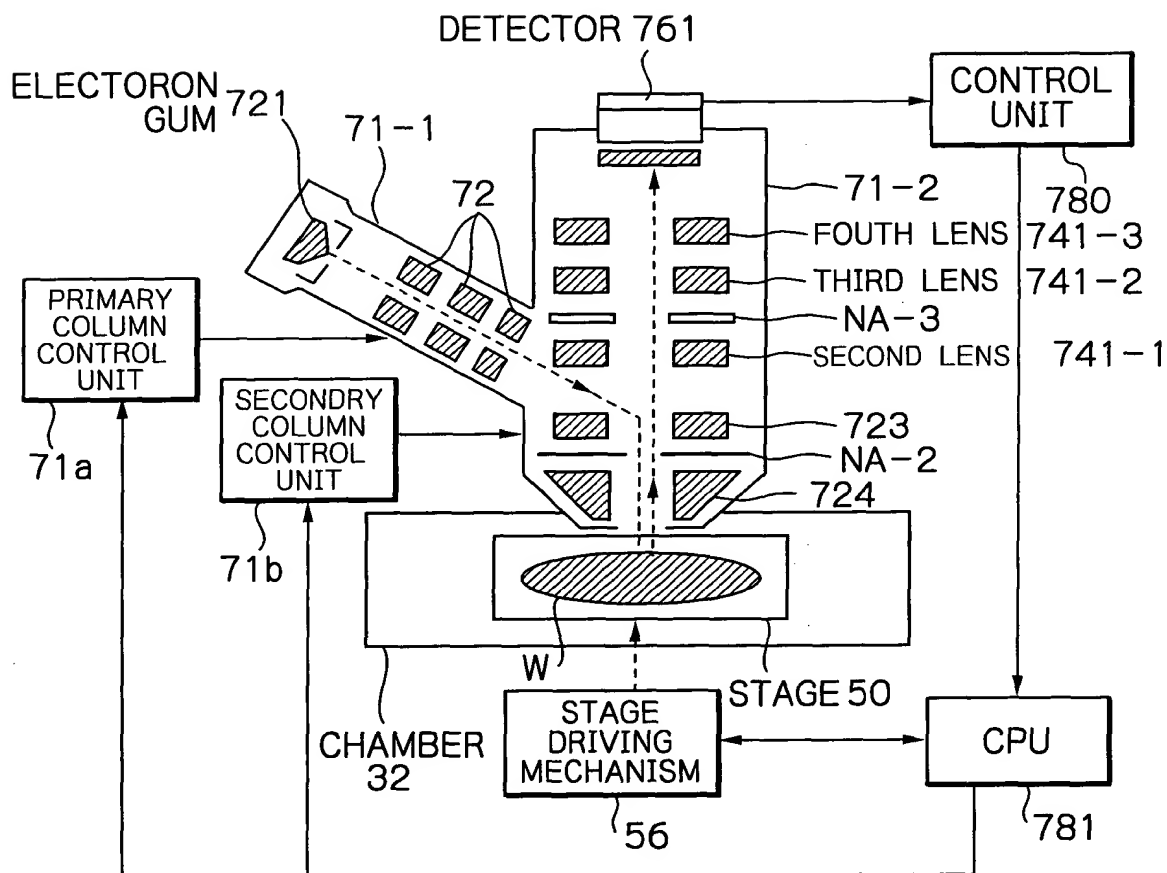


*Fig. 10*



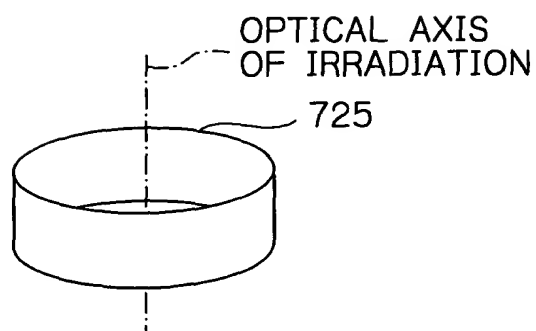
3910

Fig. 11

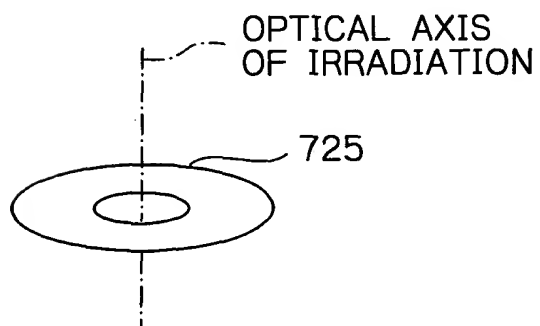


3315

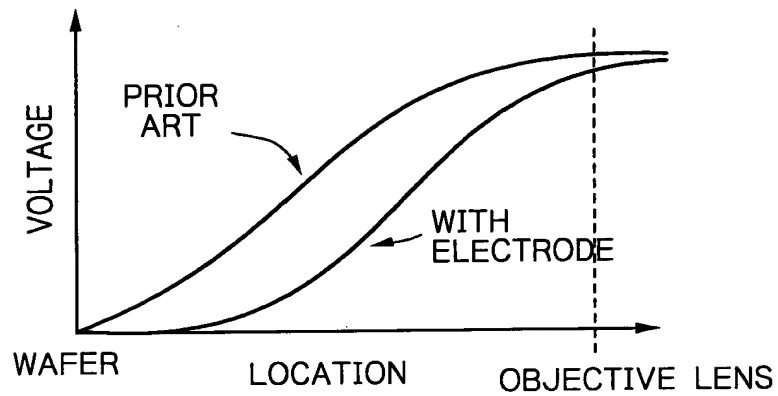
*Fig. 12*



*Fig. 13*



*Fig. 14*



*Fig. 15*

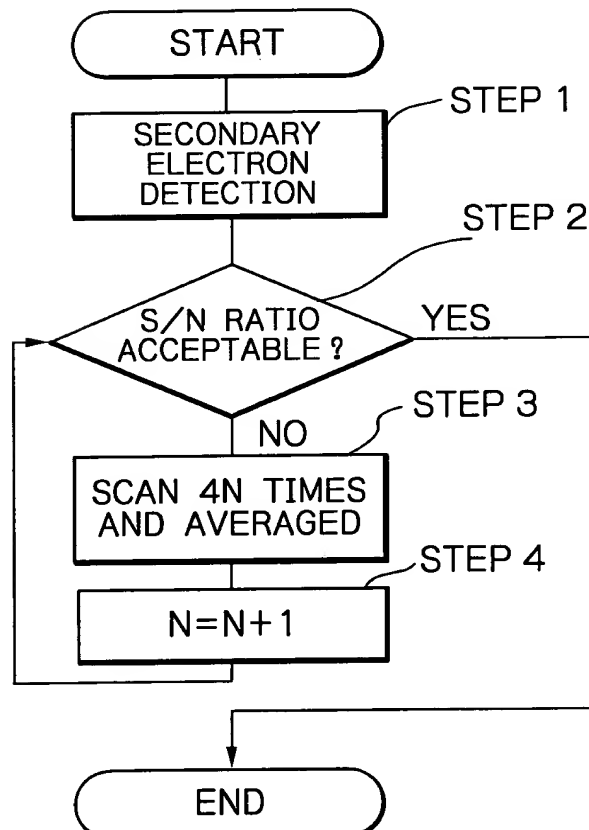
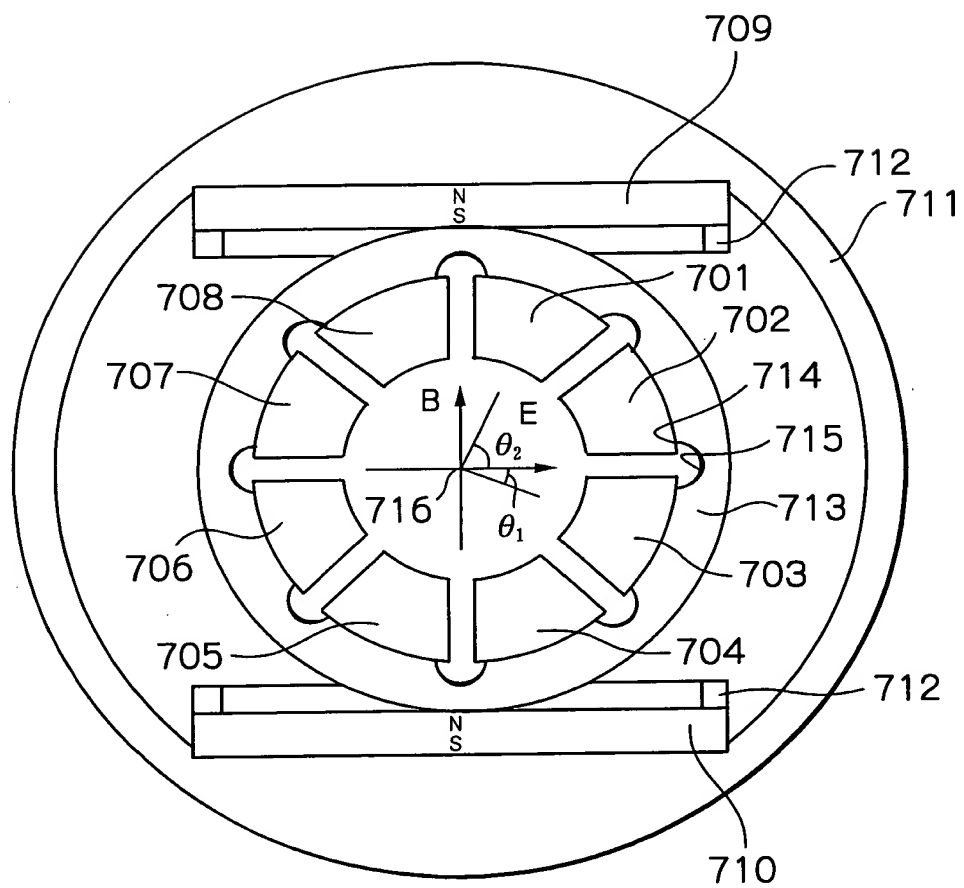




Fig. 16





3910

81

819

711

712

712

819

818

820

815

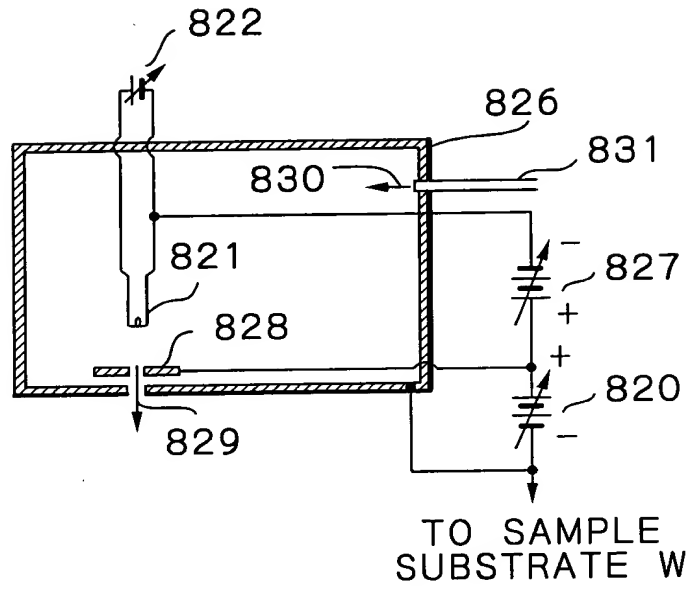
816

817

W

TO SAMPLE SUBSTRATE W

*Fig. 20*



*Fig. 21*

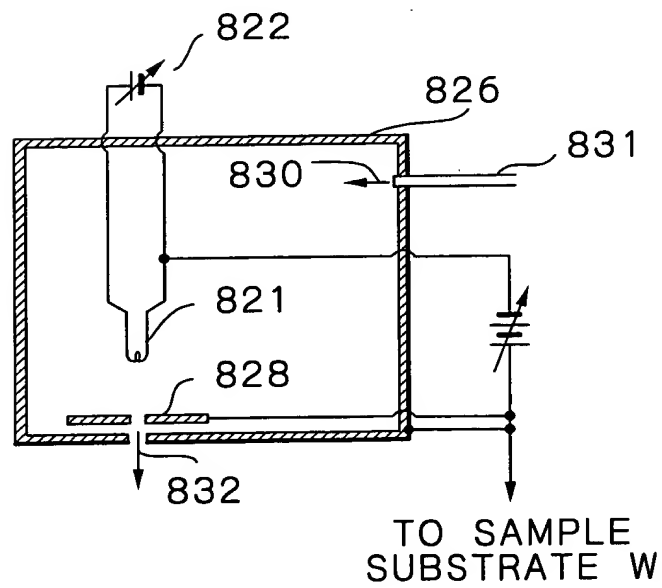


Fig. 22

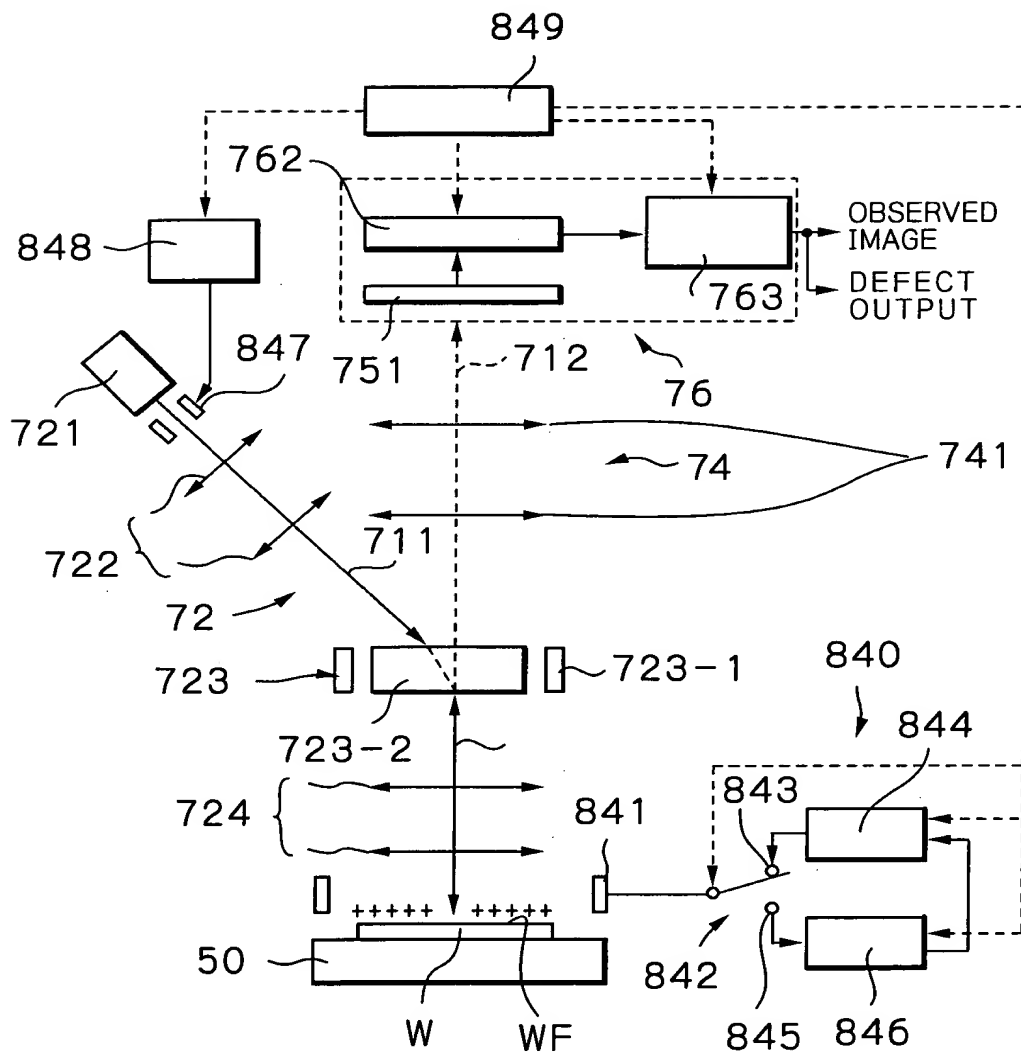
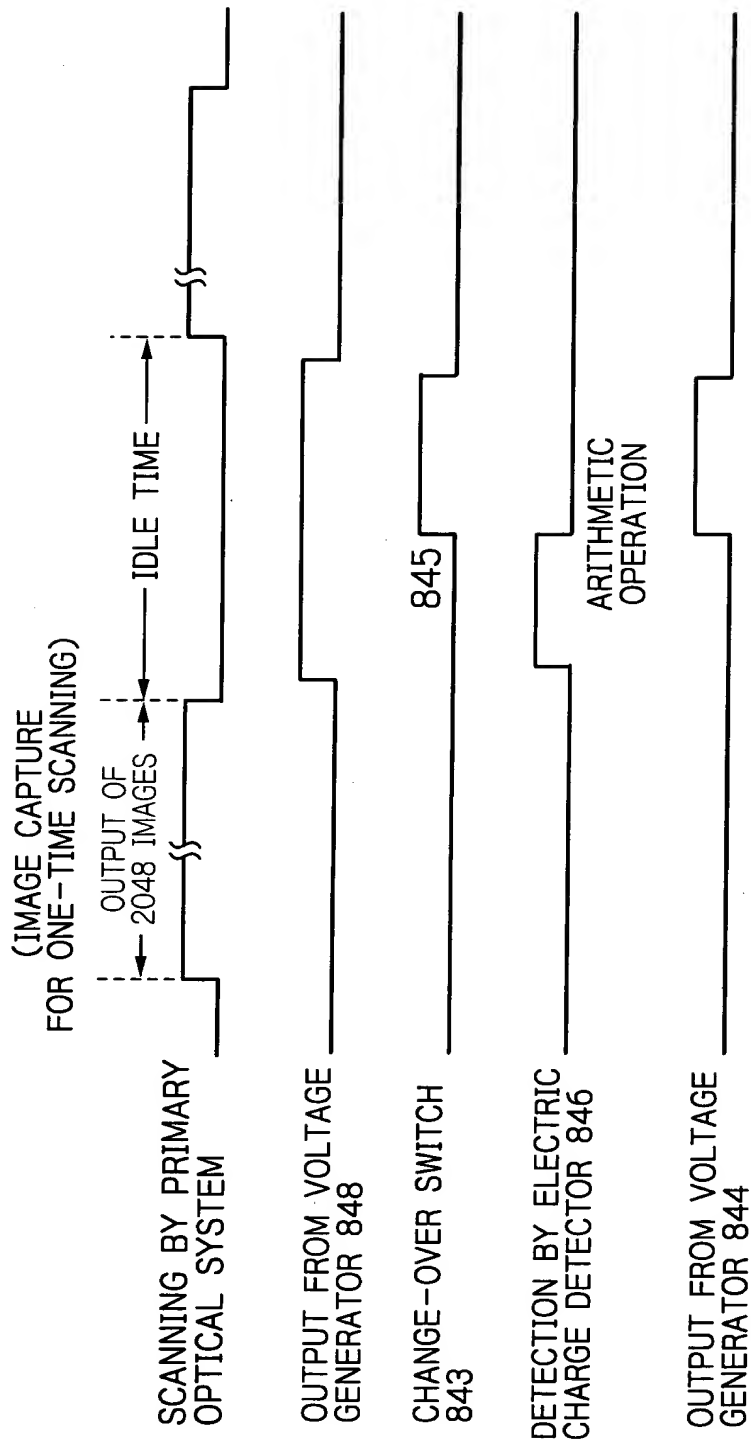
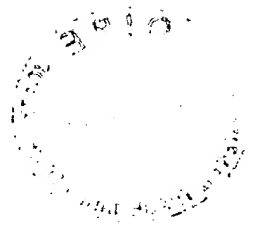


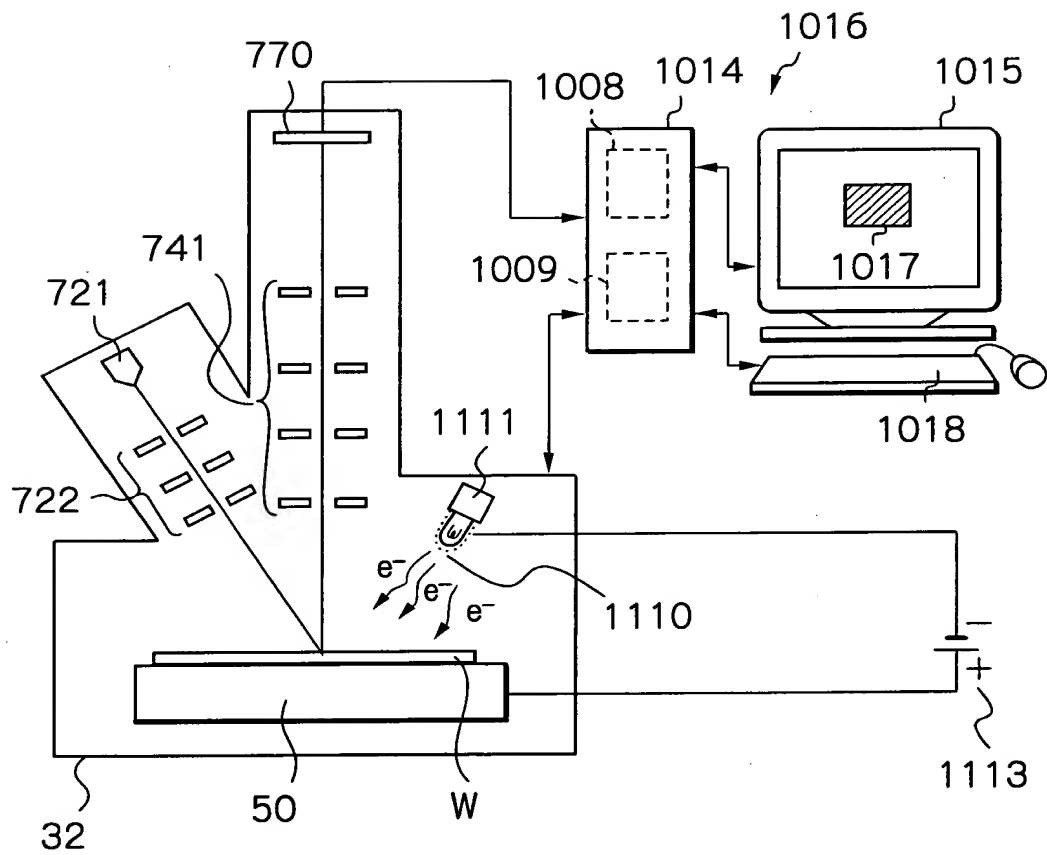
Fig. 23



009454 40204  
10204 40204



*Fig. 24*



*Fig. 25*

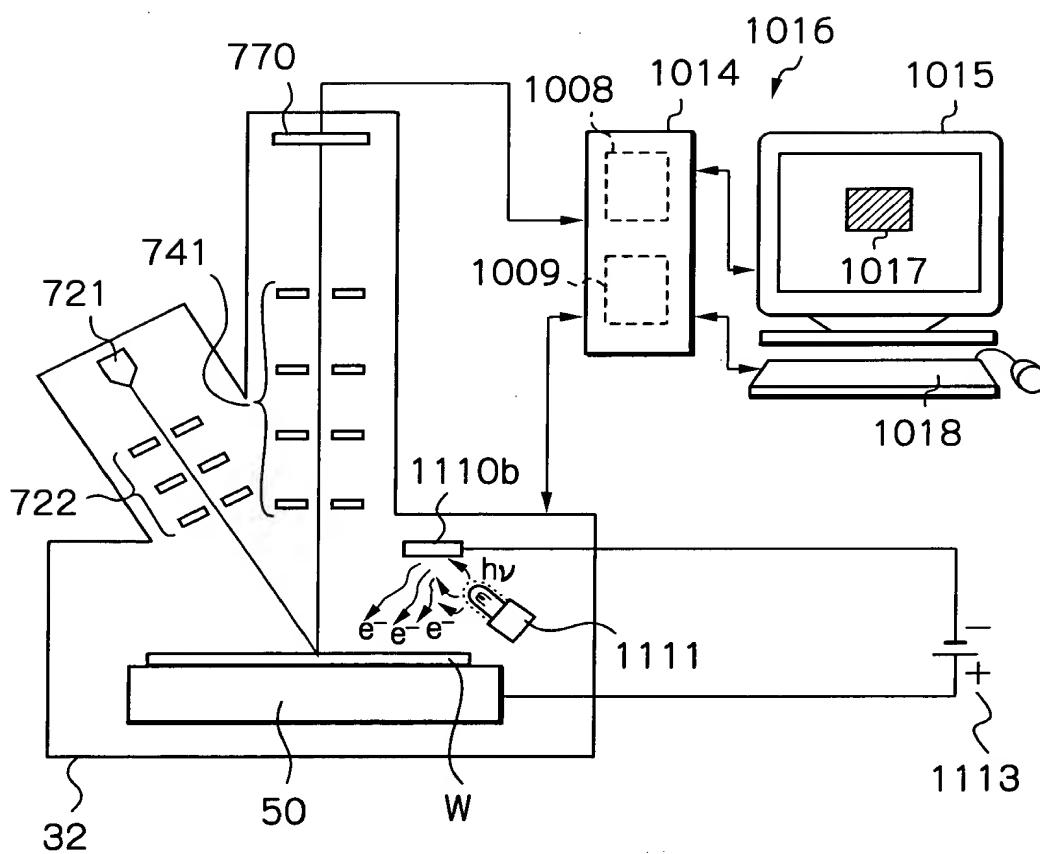
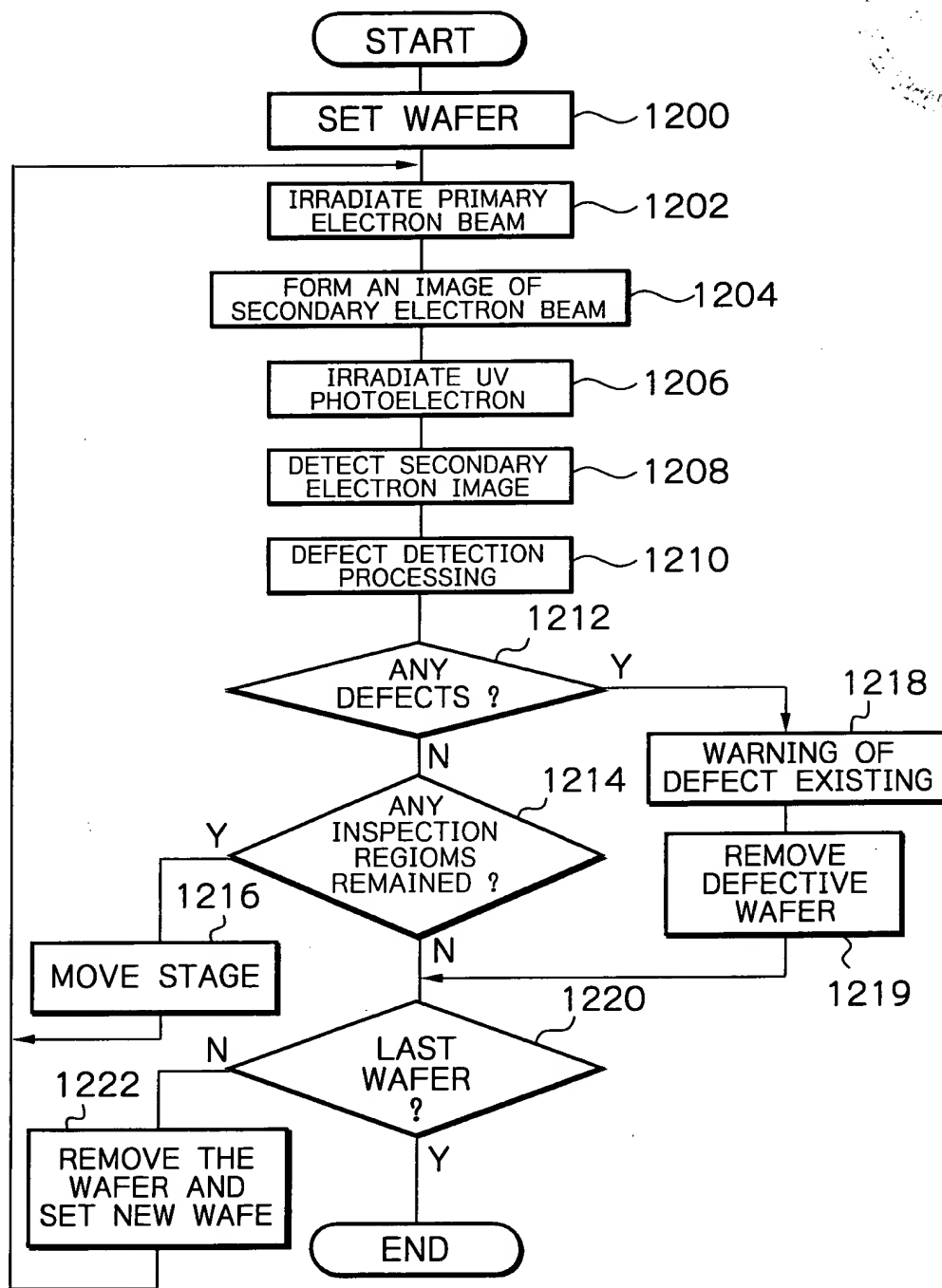


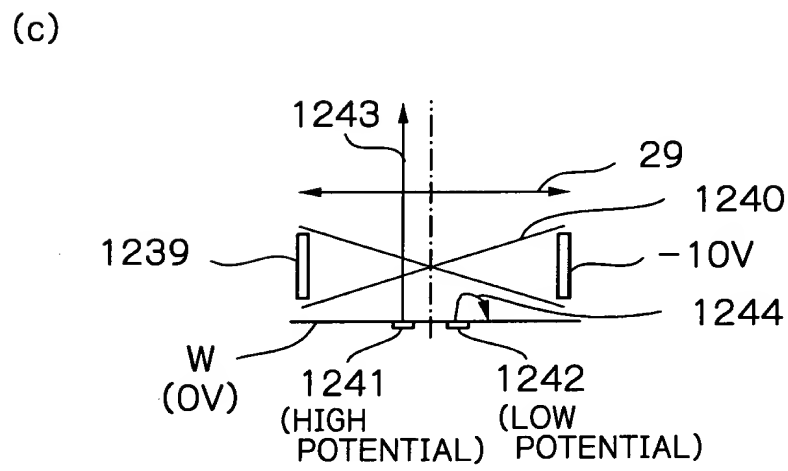
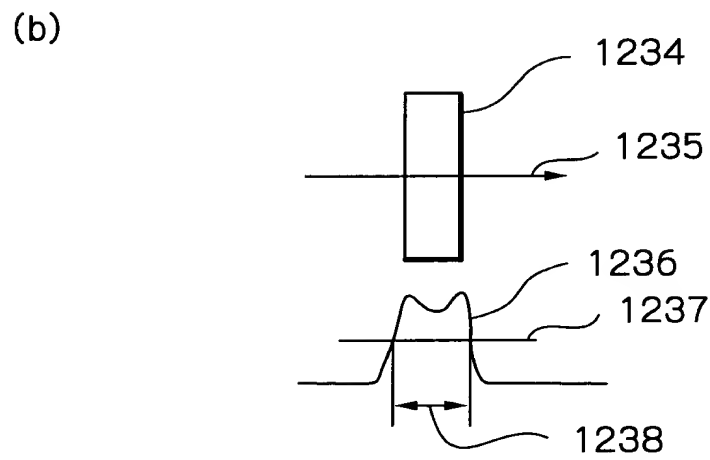
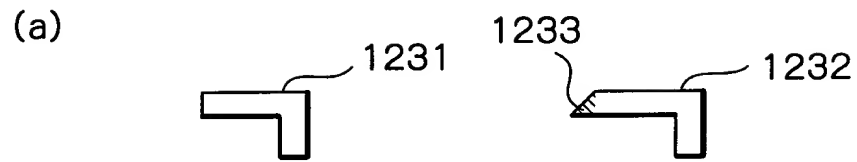


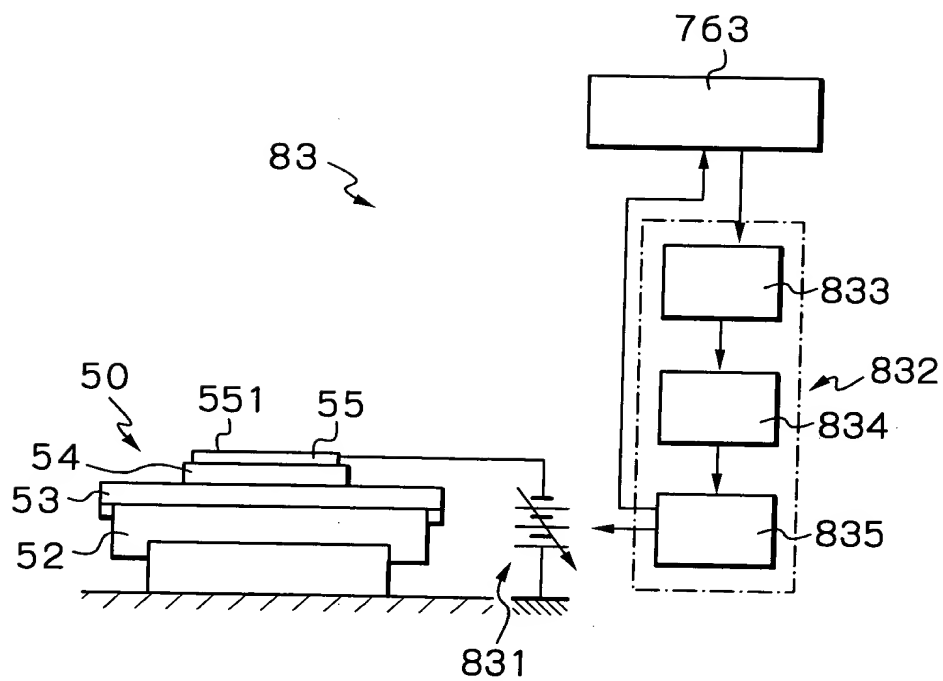
Fig. 27



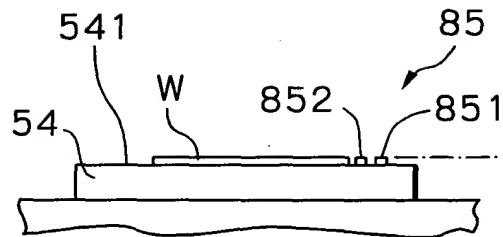
2004.4.15.000

*Fig. 28*

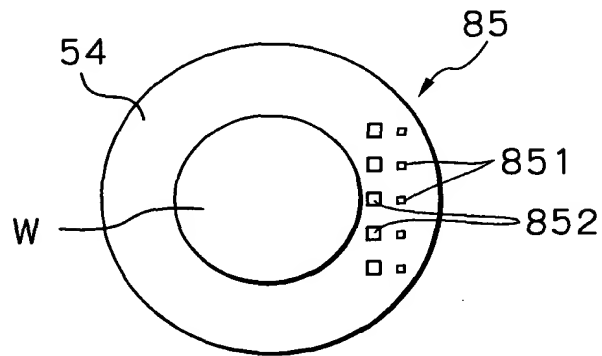


[illegible]

*Fig. 30 A*

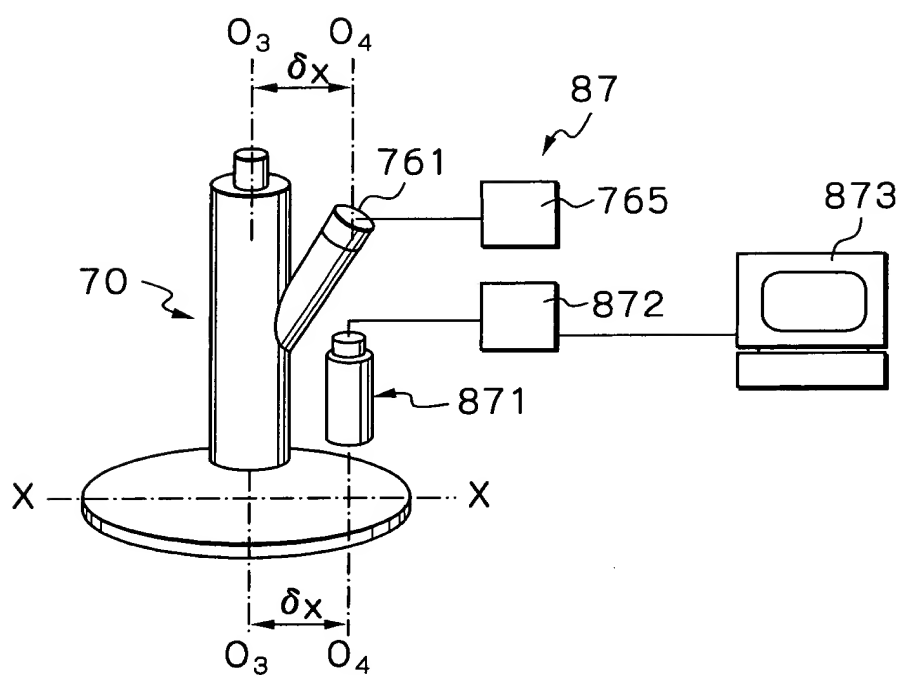


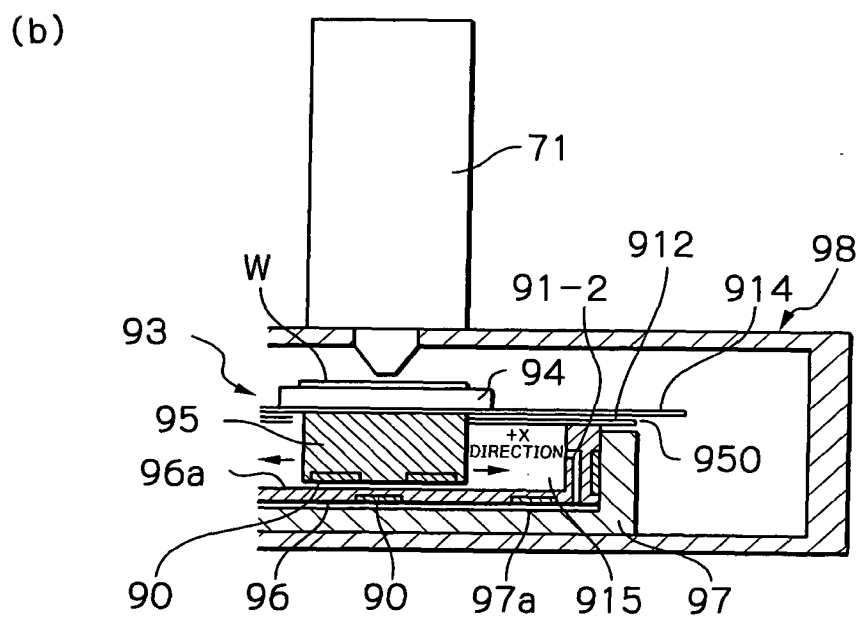
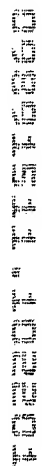
*Fig. 30 B*

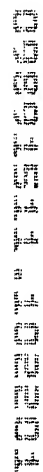
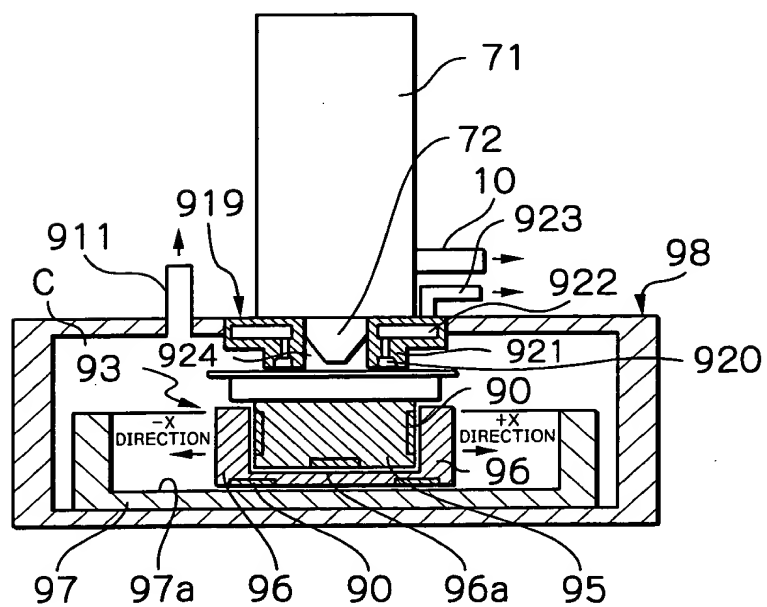


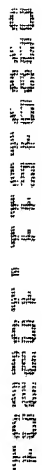


*Fig. 31*

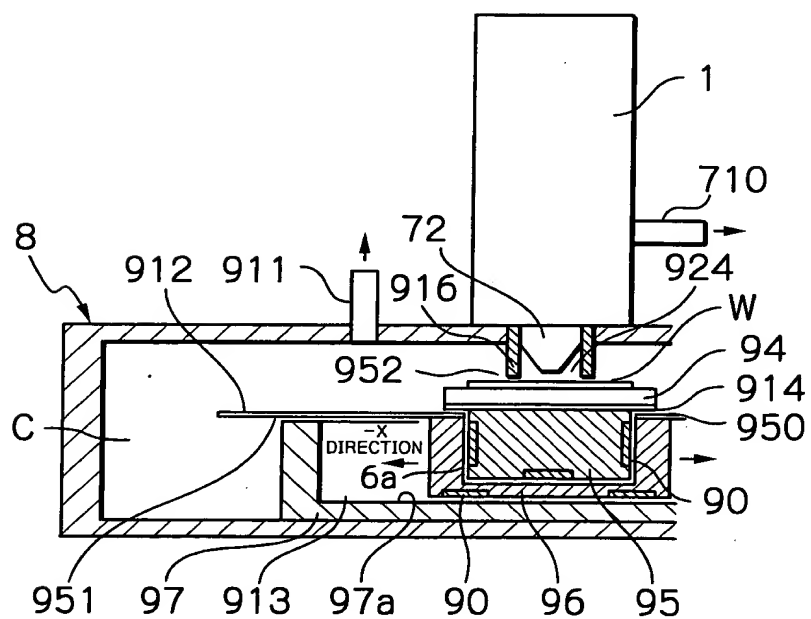




[illegible]



*Fig. 36*





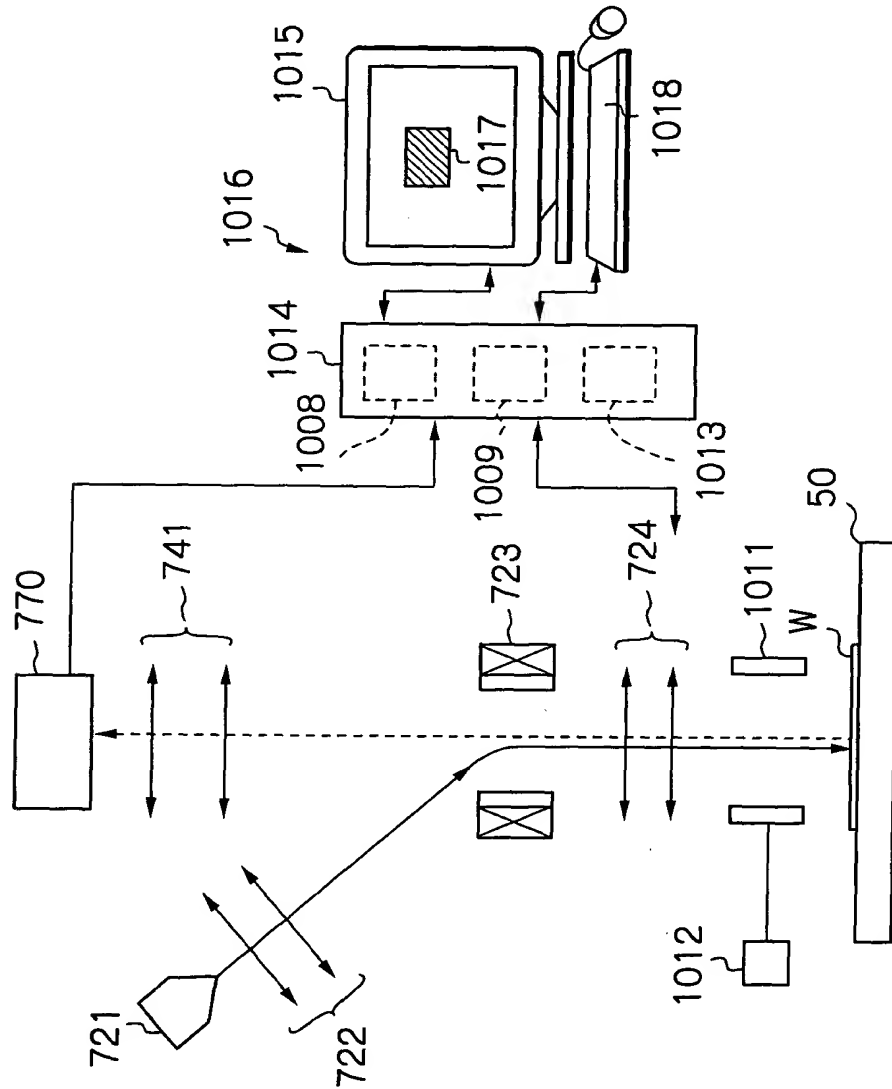






This diagram illustrates a second embodiment of the optical system. It features a light source 760 at the top, which emits light through a rectangular component 761. The light path is indicated by a dashed vertical line. A series of horizontal double-headed arrows, labeled 741, represent light rays or wavefronts. A bracket 74 groups these rays. The light then passes through a series of components labeled 723-1 and 723-2, which are part of a larger assembly 72. A component 721 is shown at an angle, reflecting light towards the main assembly. A bracket 722 indicates a specific region. The light then passes through a component 730. At the bottom, the light is directed towards a substrate 50, which has a layer W on its surface. A bracket 724 indicates a region near the substrate.

Fig. 41



FOOT F15F660

Fig. 42

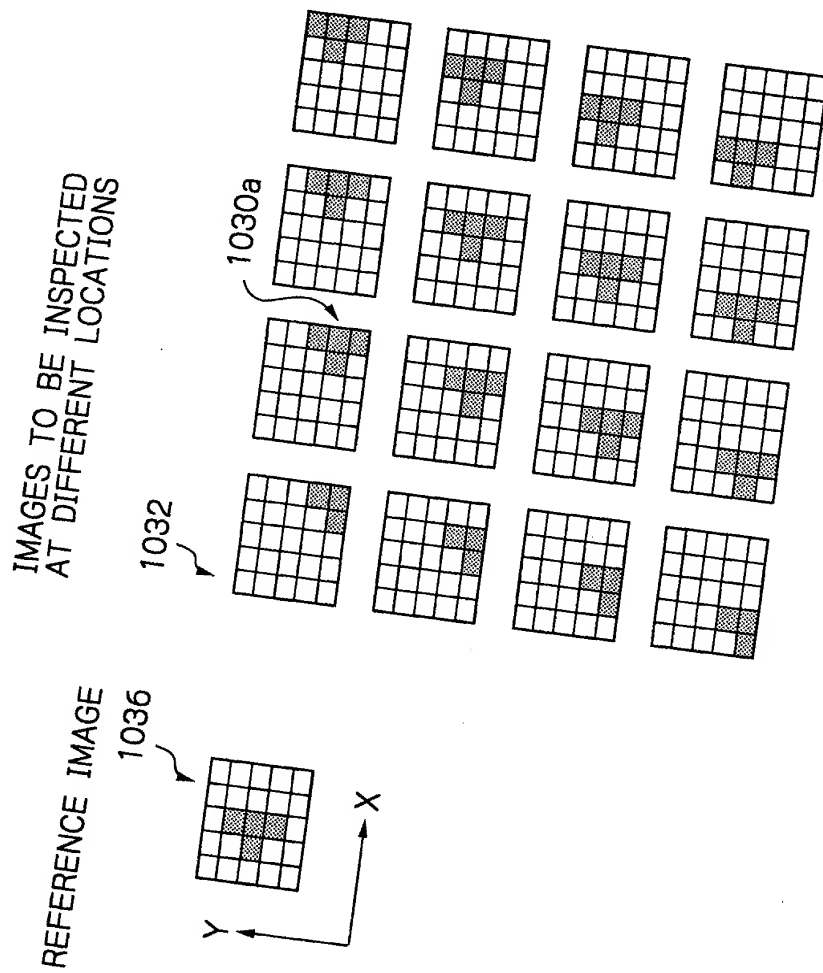
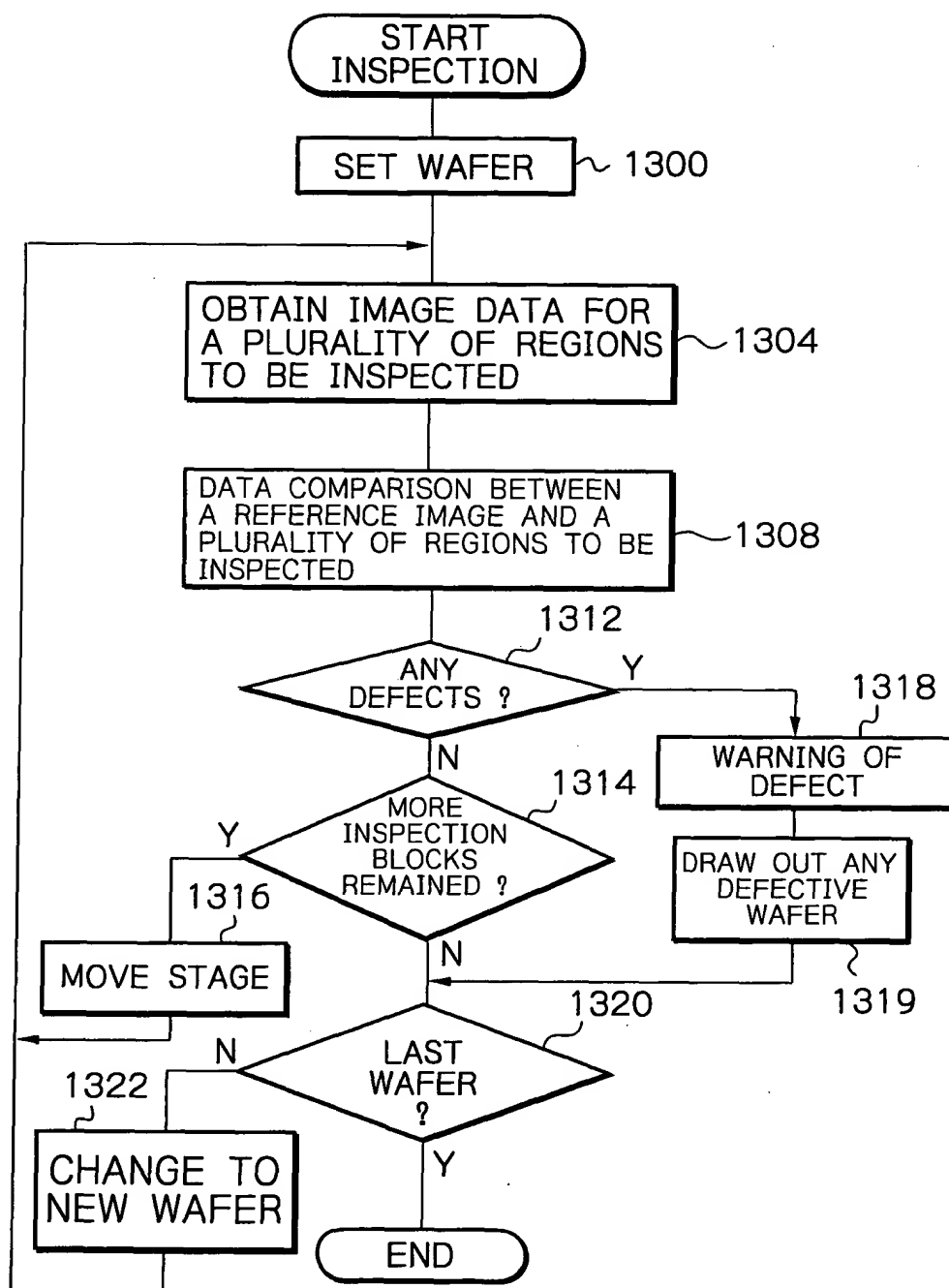


Fig. 43



0004154 0004

Fig. 44

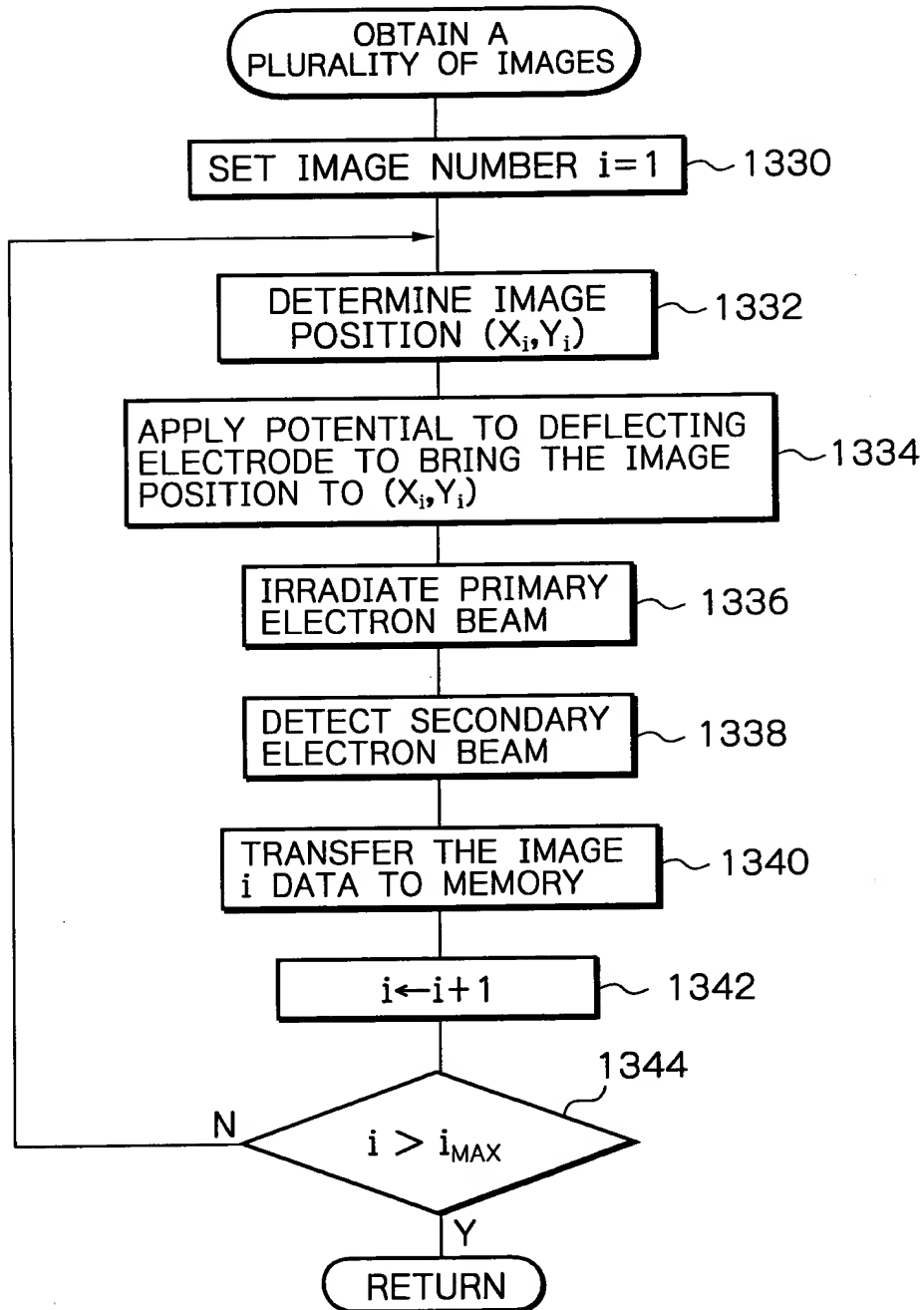
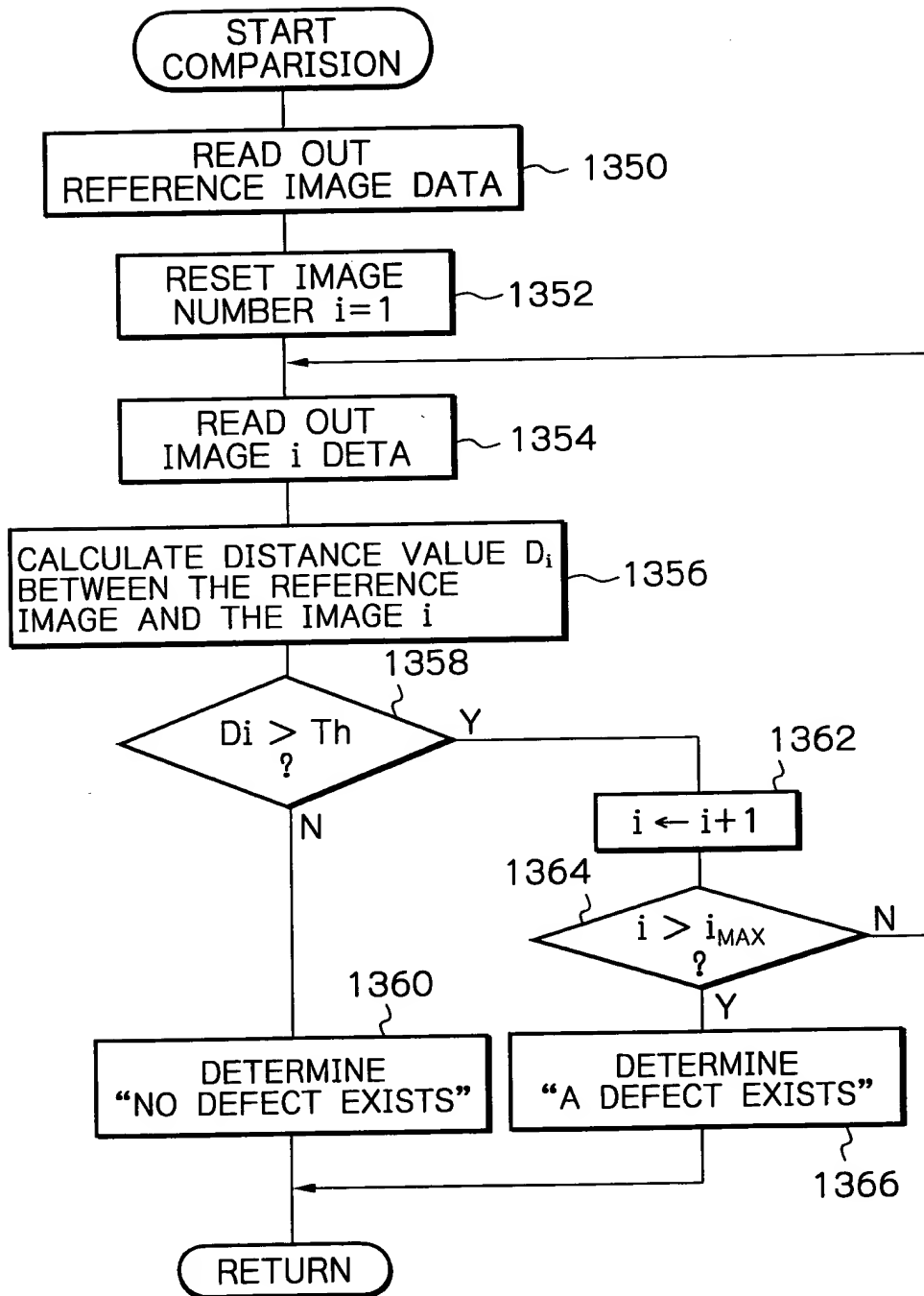
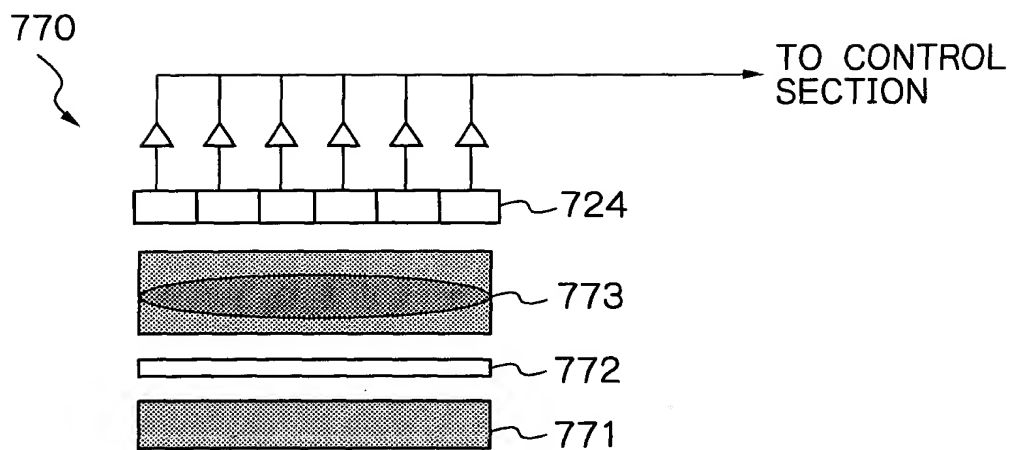


Fig. 45





*Fig. 46*



*Fig. 47*

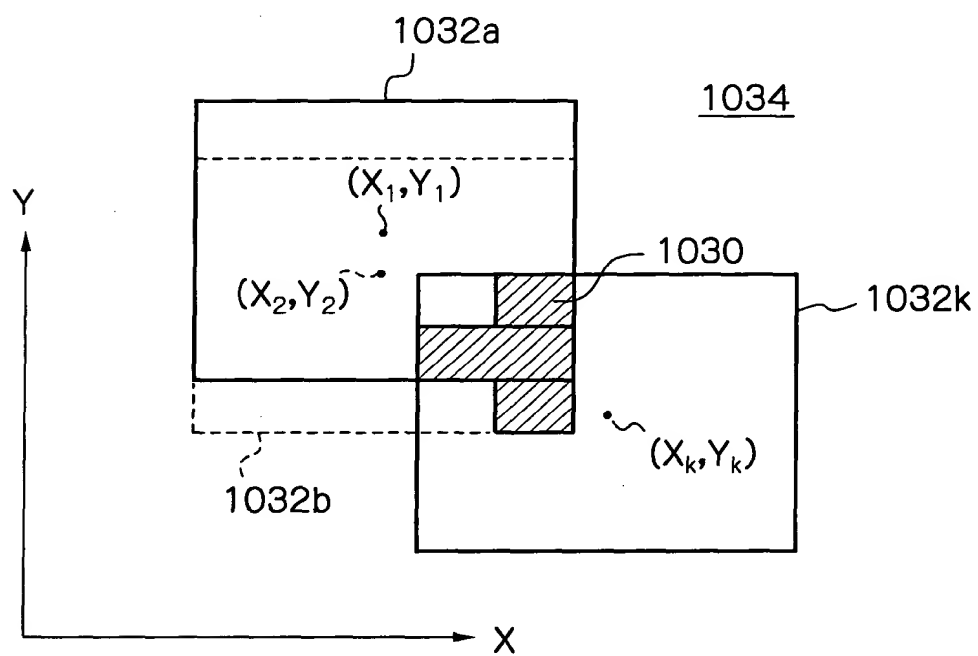


Fig. 48 A

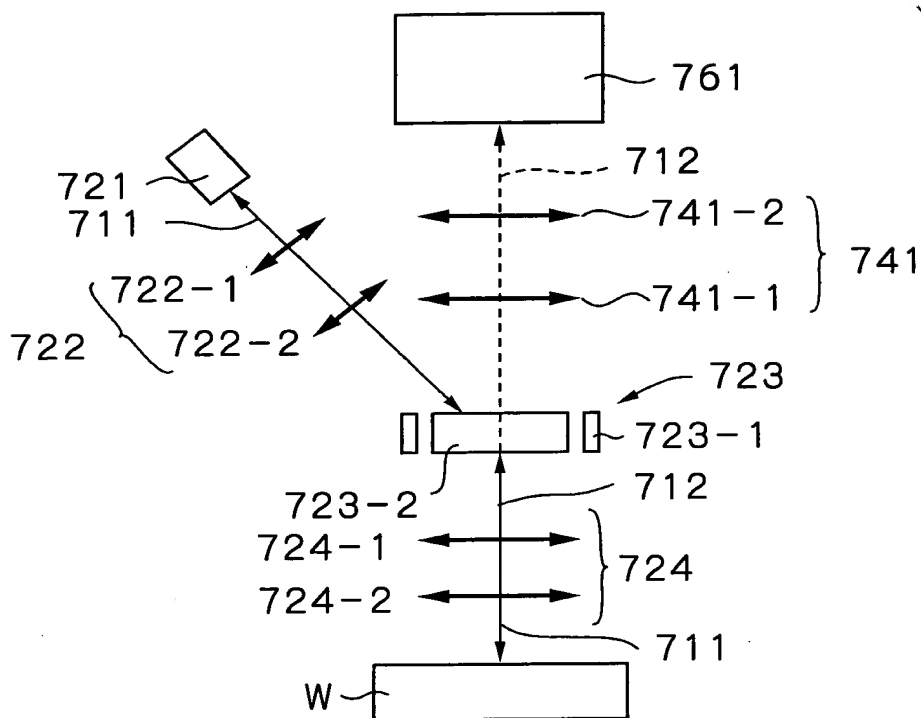


Fig. 48 B

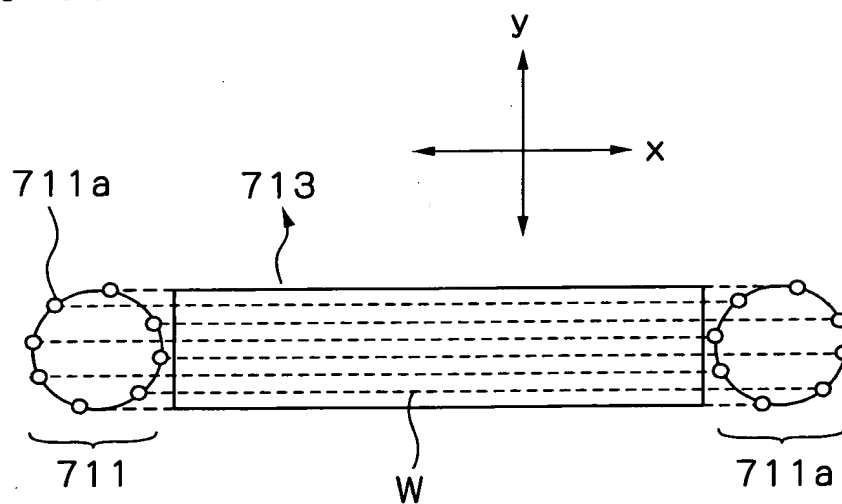


Fig. 49 A

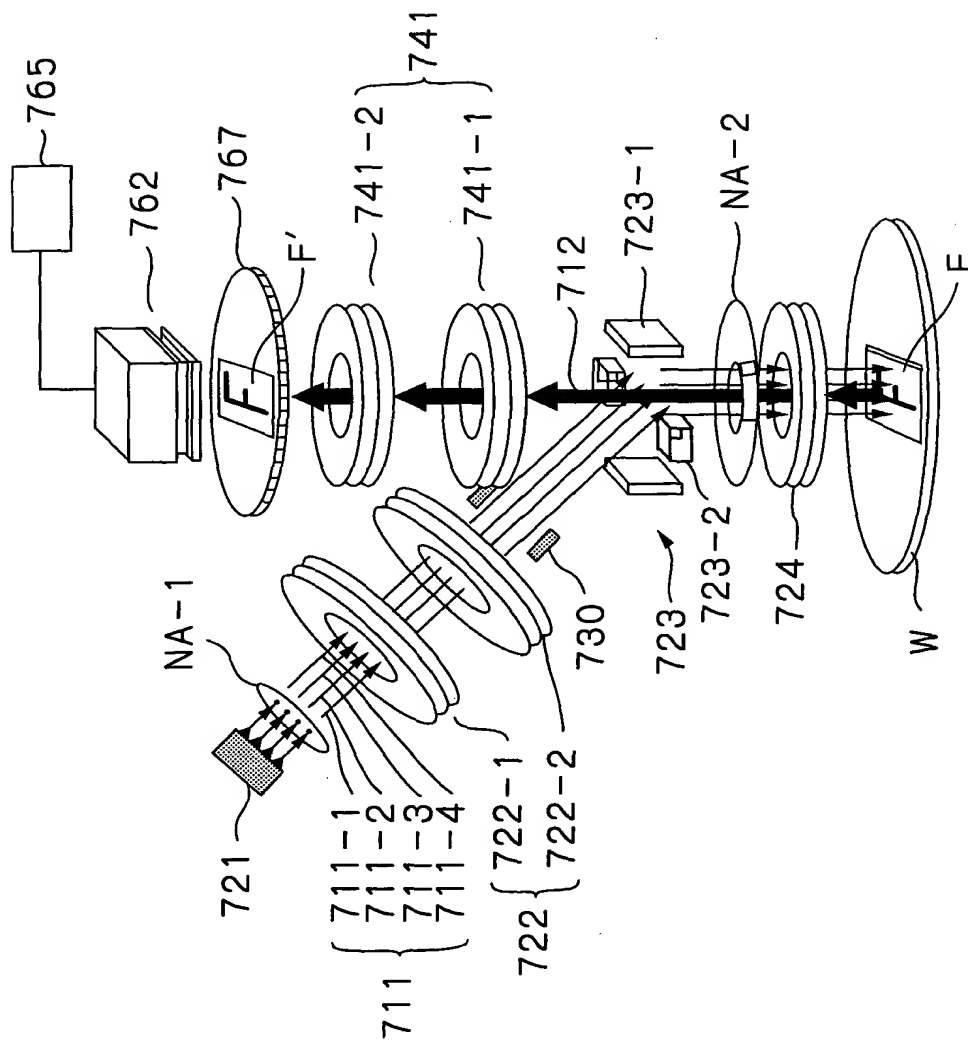
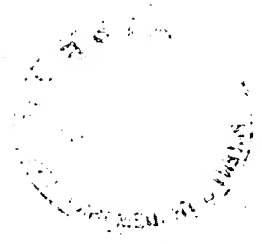
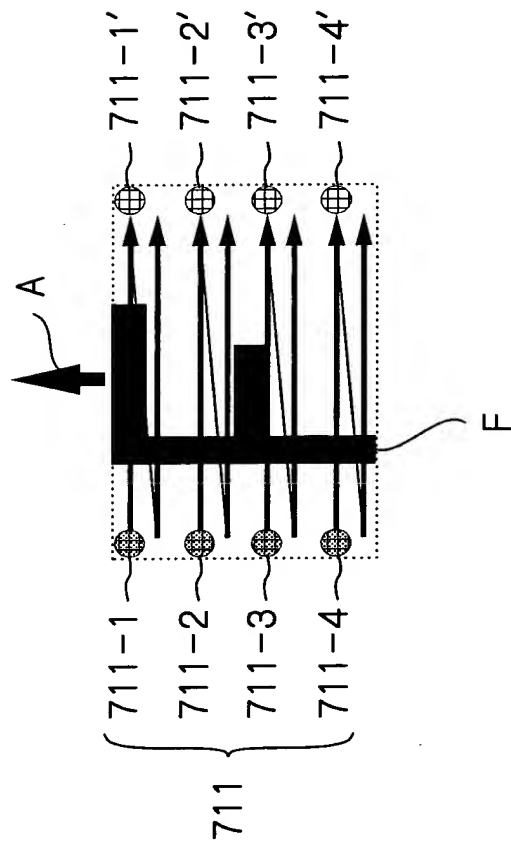
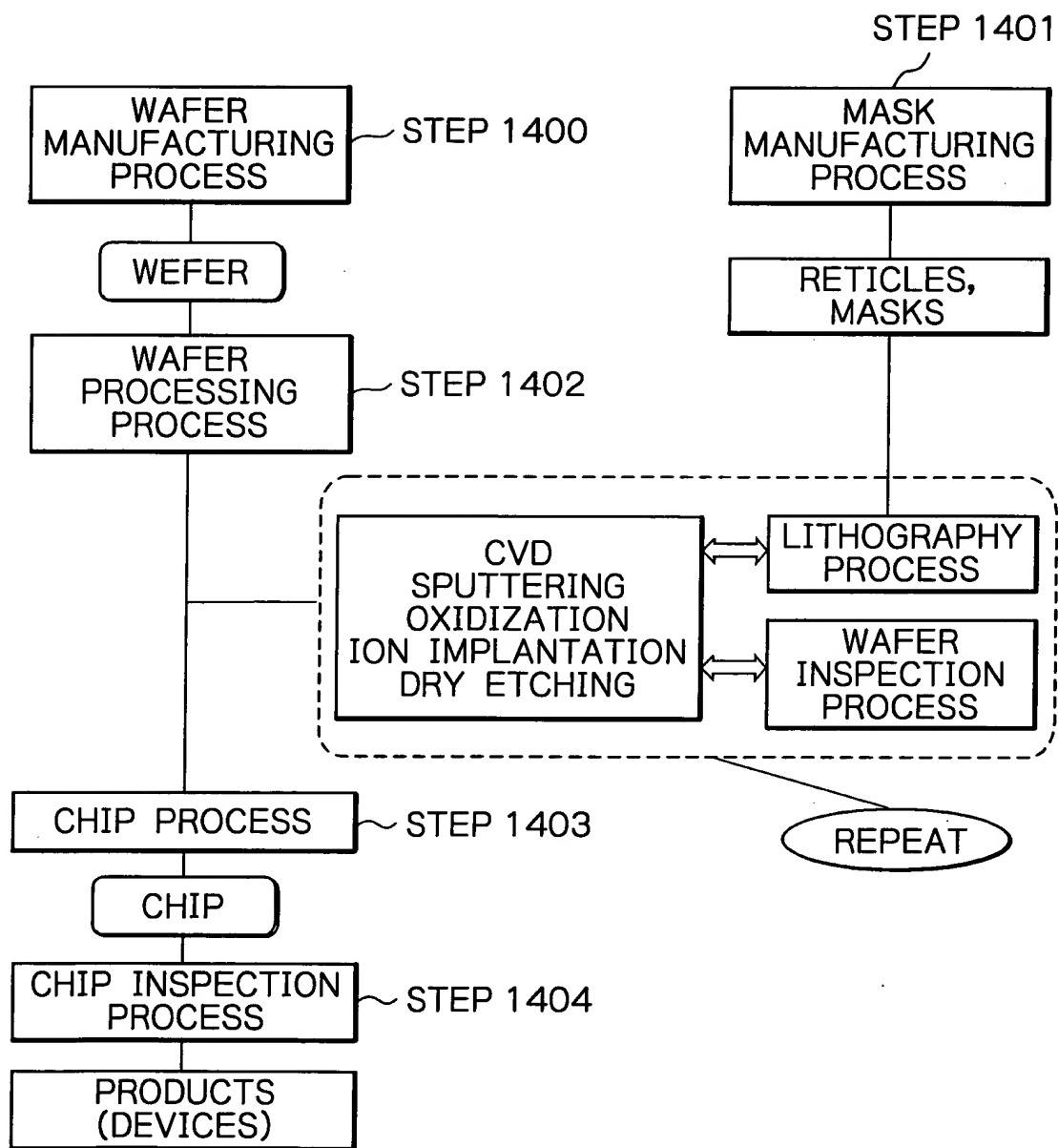


Fig. 49 B



*Fig. 50*

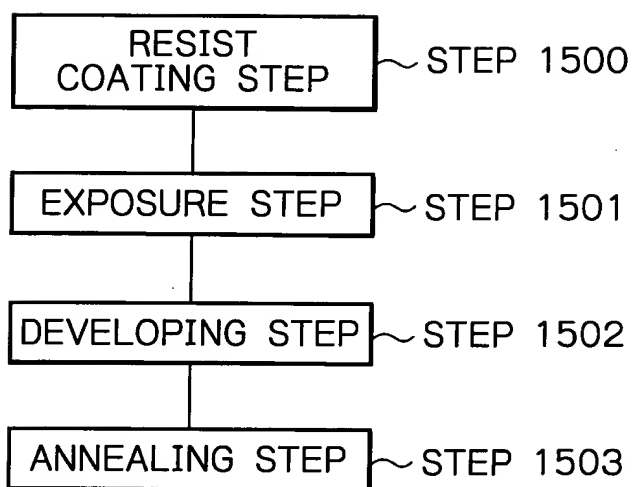


```

graph TD
    A[RESIST COATING STEP] --> B[EXPOSURE STEP]
    B --> C[DEVELOPING STEP]
    C --> D[ANNEALING STEP]
  
```

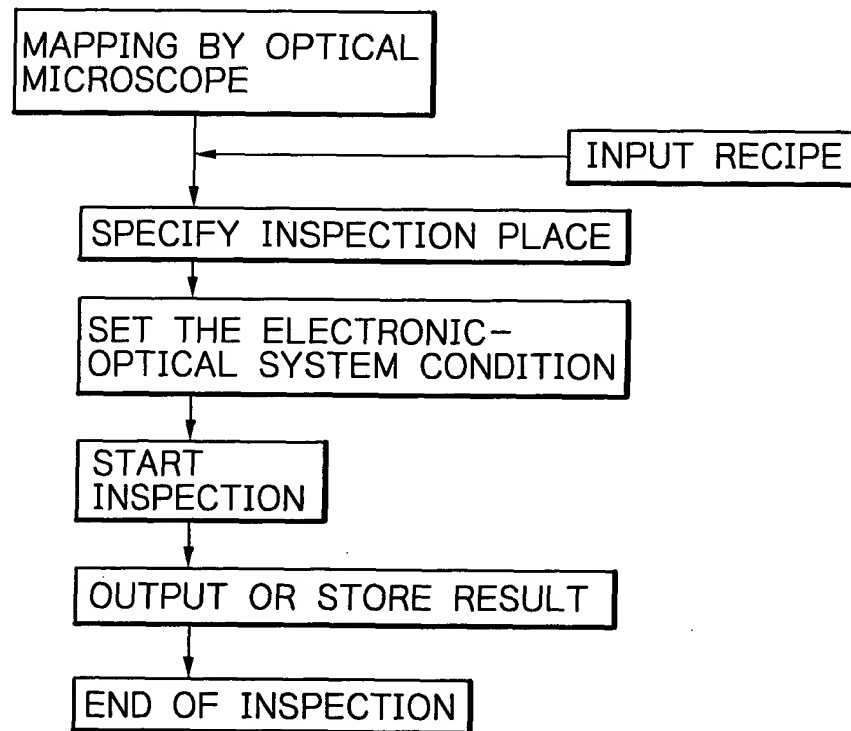
The flowchart illustrates the sequence of steps in the lithography process:

- RESIST COATING STEP (STEP 1500)
- EXPOSURE STEP (STEP 1501)
- DEVELOPING STEP (STEP 1502)
- ANNEALING STEP (STEP 1503)



[illegible]

*Fig. 51B*



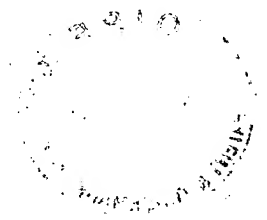
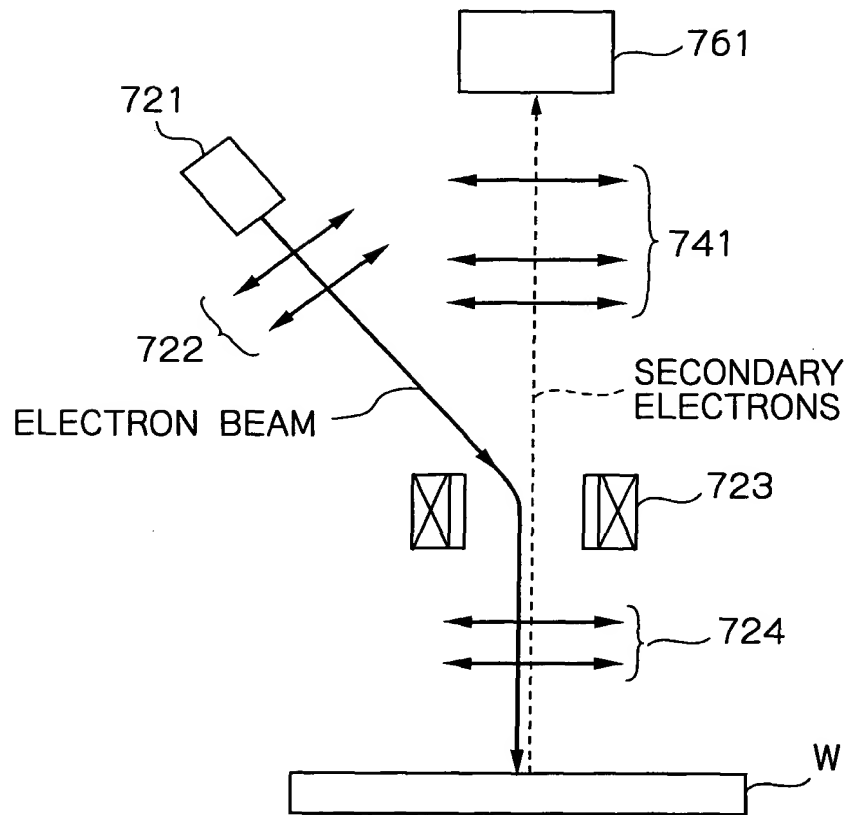
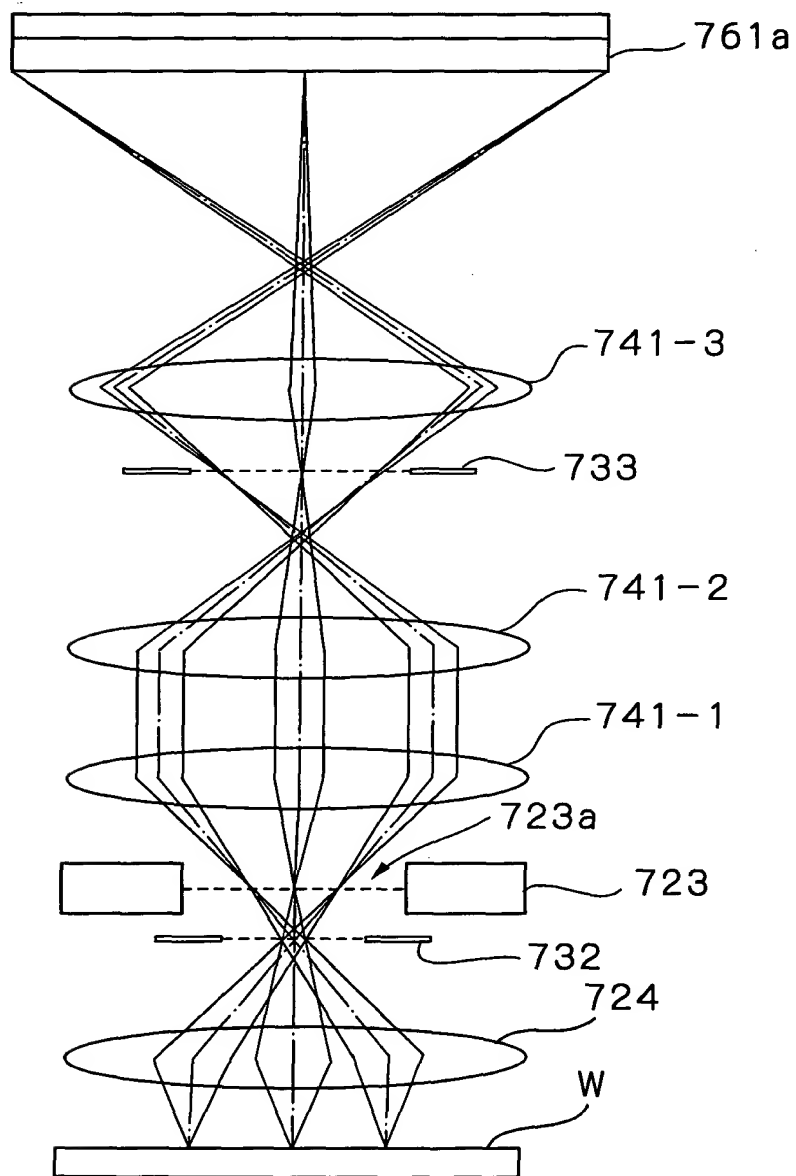


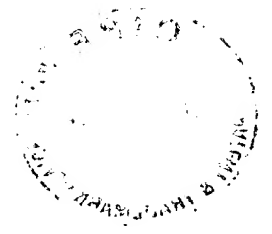
Fig. 52



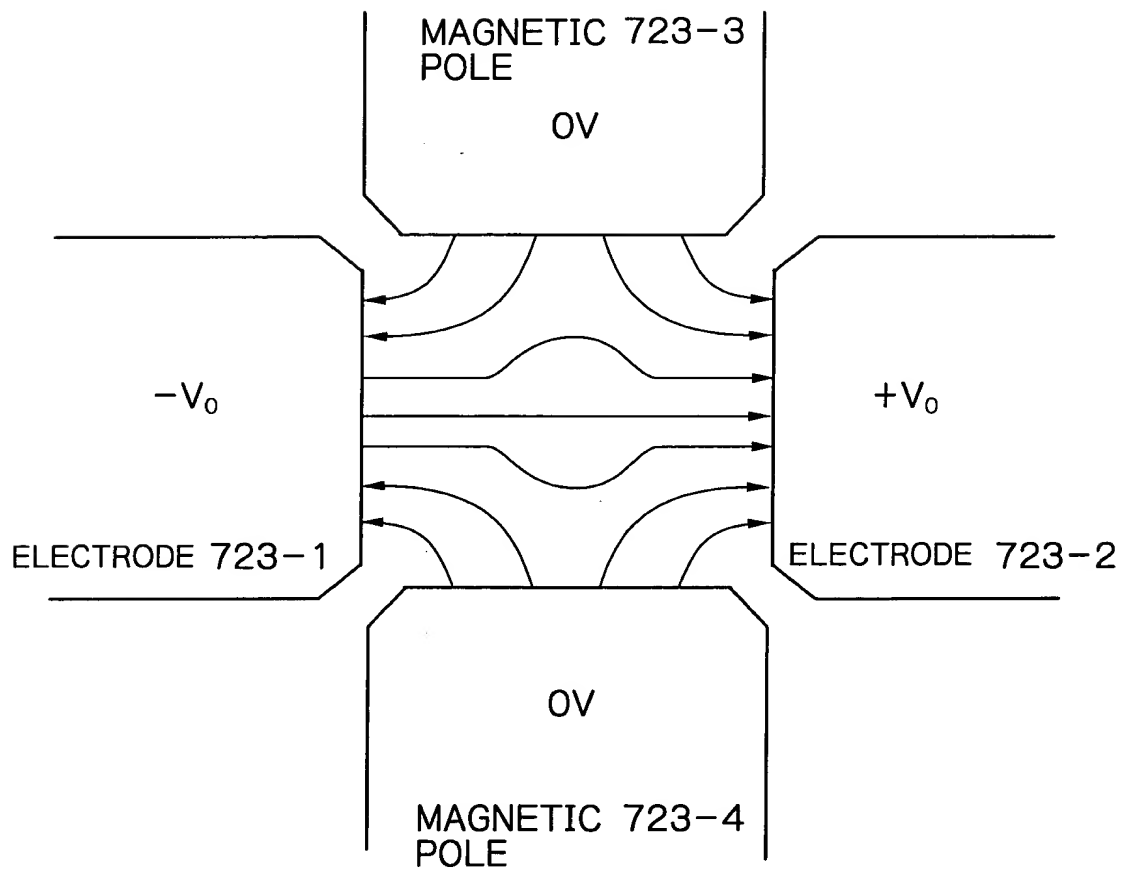
0999454 109994

Fig. 53



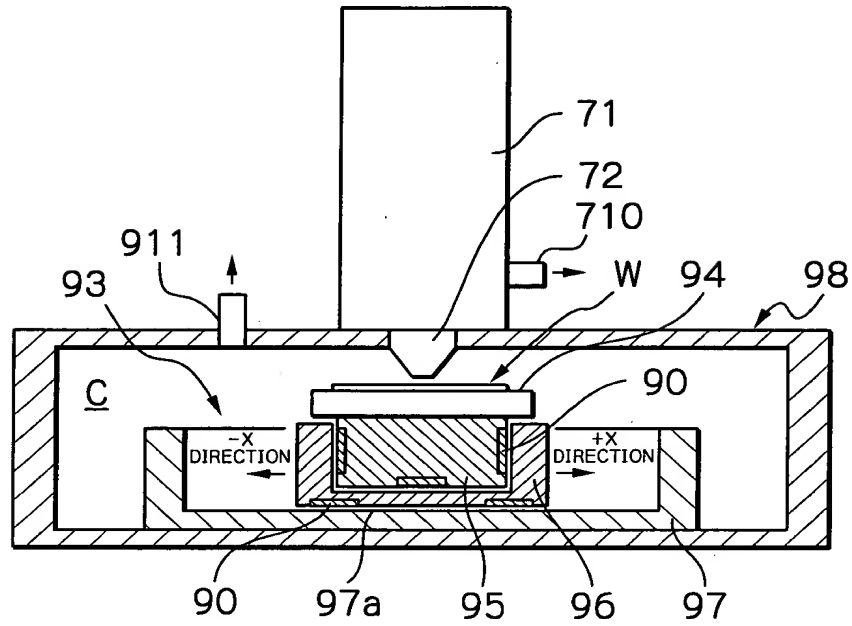


*Fig. 54*

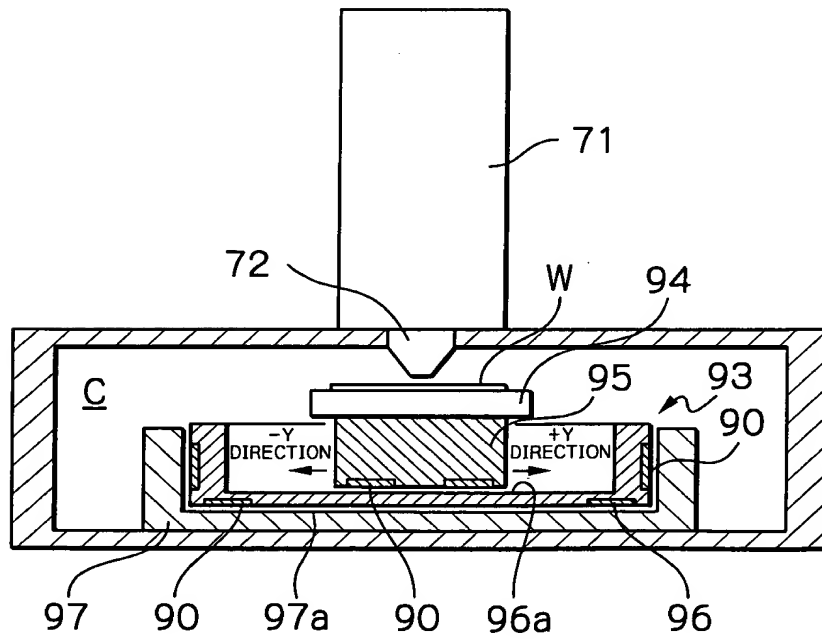


*Fig. 55*

(a)



(b)



*Fig. 56*

